Bio-Data

1. **Name**   :   NIRANJAN N. CHIPLUNKAR

2. **Qualification**   :   B.E, M.Tech, Ph.D

3. **Sex**   :   Male

4. **Marital Status** :   Married

5. **Date of Birth**   :   27thDecember 1964

6. **Category**   :   General

7. **Nationality**   :   Indian

8. **Present Position**   :   Principal

                                                            NMAMIT, Nitte

9. **Address for communication**   :

                        Principal

                        NMAMIT,

                        P.O. Nitte, Taluq: Karkala

                        Dist: Udupi

                        KARNATAKA, Pin- 574 110

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                        Mob   : 9448381473

                        Personal Email: niranjanchiplunkar@rediffmail.com
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                        Official email: principal_nmamit@nitte.edu.in

10. **Academic Record:**

   Graduation   :   B.E in E&C from NIE, Mysore, India, 1986 -(FCD)

   Post-Graduation :   M.Tech in CSE from MIT, Manipal, India, 1990-(FCD)

   Doctorate   :   Ph.D in CSE from University of Mysore, India, 2002

Area of Research   :   CAD for VLSI

   Topic   :   Design of Algorithms for optimum area utilization in VLSI ASICS

11. Experience:

From	To	Organization	Designation	Total Period
1987	1993	Manipal Institute of Technology (MIT), Madhav Nagar Manipal, Karnataka, 576104	Lecturer	6 years
1993	1996	Manipal Institute of Technology (MIT), Madhav Nagar Manipal, Karnataka, 576104	Reader	3years
1997	2003	NMAMIT, P.O: Nitte Karkala Taluk, Udupi District, Karnataka, India-574 110	Professor	7 years
2003	2012	NMAMIT, P.O: Nitte Karkala Taluk, Udupi District, Karnataka, India-574 110	Professor & Head of the Department	9years
2010	2012	NMAMIT, P.O: Nitte Karkala Taluk, Udupi District, Karnataka, India-574 110	Dean(Academics)	
13th June 2012	Till date	Principal NMAM Institute of Technology, Nitte		
Total				**35years**

12. Membership in Professional Bodies

- Life Member of ISTE (LM816)
- Senior Member of IEEE (01492834)
- Senior Member of CSI (F8000642)
- Fellow of IE(India)
- Life Member of Indian Society of Systems for Science & Engg.

13. **Awards and Recognitions**

- Received Cogent Engineering Best Paper Award, Paper titled "Real time Twitter data Analysis using Hadoop Ecosystem" for the publication during 2017 and 2018, Taylor and Francis Publication.

- Received Education Excellence award for NMAMIT during Indian Technological Congress Convention held from 5to 6 September 2018 at Nimhans Convention Center Bangalore from Honorable Chief Minister of Govt. of Karnataka Sri H.D.Kumaraswamy

- " Bharatiya Vidya Bhavan National award for Best Engineering College Principal 2014" given by ISTE

- "Excellent Achievement Award" by Centre for International Cooperation on Computerization (CICC), Govt. of Japan in 2002

- "Outstanding Alumnus of MIT, Manipal", by ManipalUniversity, Manipal, India in 2008

- Resume is published in "Marquis Who's Who in World" in January 2009

14. **Overseas Experience**

- Attended a training on "Web Based Learning" at Tokyo, **Japan**, **from Jan, 2002 to Mar, 2002**.

- Participated in Educational Fair at Colombo, **Srilanka**. **Aug,2004.**

- Participated in Educational Fair at **Abu dhabi**., UAE**Feb, 2006.**

- Participated in School Presentations at Abu dhabi, **Dubai and Sharjah** , **Dec, 2006**.

- Personal Visit to **Malaysia** and **Singapore** during **Dec. 2007**

- Attended EMC World Conference in **Las Vegas,USA** from **19 to 22 May 2008**, with assistance from EMC, TEQIP and NET

- Visited **Stanford University, University of California at SanDiego and California State University in SanDiego** during **May 2008**

- Visited **Doha, Qatar** during **2009 April and June**on official assignment

- Presented a paper in ICCNT-2010 at **Bangkok, Thailand**on **24 April 2010**

- Presented a paper in ICCRD-2011 at **Shanghai, China** from 11 to 13 **March 2011**

- Presented Poster paper on "Intel Embedded Systems lab. at NMAMIT, Nitte" in **Penang, Malaysia** during Intel Academic Forum **2012**.

- Presented an invited paper on "Industry Institute Interaction in India" at **Ritsuimeikan University Japan** during **March 2013**

- Attended **GEDC Conference at Chicago** from 20 to 22 **October 2013**.

- Visited **Purdue University, Michigan State University and University of Wisconsin from** 23 to 25 **October 2013**

- Attended NMAMIT Alumni Meet in **Dubai** during **March 2015**

- Attended Workshop on "Nano Satellites" at Tel Aviv Technological University**, Israel** during **May 2019**.

- **Visit of Nitte Delegation to Japan** during **November 2019.**

- Attended NMAMIT Alumni meet in **Dubai** during **January 2020.**

15. Co-Curricular Activities

15.1 Lectures Delivered

- Talk on "Äutomation in IC Design" for the CSI Student branch, NMAMIT, Nitte on 21 January 2016

- IUCEE Virtual academy webinar "effective use of information and communication technology in enhancing teaching learning outcomes", Tuesday on 5 January 2016, from 3.30 PM to 4.30 PM

- Lecture on "Research Issues in CSE" at Infosys campus, Mangalore on 30 March 2014

- Lecture on "Engineering Education" during the "Education & Career guidance program" arranged by "The Hindu" at Mangalore on 27 May 2012

- Lecture on "Applications of Graph theoretic algorithms in computer Science Domain - CAD FOR VLSI" at MVSIT, Moodabidri on 17 March 2012 on the occasion of 5 days VTU-VGST sponsored Faculty Development Program
- Lecture on "What an IT Engineer must be good at ? " at Sode Madhva Vadiraja Institute of Technology and Management (SMVITM) , Bantakal on 1 March 2012
- Lecture on "Tackling barrier factors for a quality research; Application of Signal analysis and computer imaging for non-invasive diagnosis" on 9 February 2012 at A B Shetty Memorial of Dental Sciences, Deralekatte, Mangalore – organized as part of workshop on "Need of Valid Research for Appropriate Clinical Care" by Dept. of Oral Biology
- Lecture on Lecture on "Device Drivers" at E&C Department of NMAMIT, Nitte on 8 February 2012
- Lecture on "Software issues for electrical engineering students" organized by Elites on 6 February 2012 at E&E dept. of NMAMIT, Nitte
- Lecture on "Challenges and Issues in CSE Research" at St. Joseph's Engineering College Mangalore for the faculty members of the college on 25 June 2011.
- Keynote address during the State Level Technical Paper presentation competition "Emergia 2011" organized by IEEE students chapter, Canara engg. College on 1 April 2011
- Lecture on "VLSI design and fabrication process" at Moodlakatte Institute of Technology, Kundapura on 31 October 2010
- Keynote address on "Parallel Architecture & Programming" at Faculty development program at Sahyadri College of Engineering, Mangalore on 17 September 2010
- VTU- Edusat Lecture on "Computer concepts and C Programming" from 6 - 7 September 2010 at VTU Studio, Bangalore
- Lecture on "Recent trends in communication and Computing" at Srinivasa Institute of Computer Application and Management , Mangalore during April 2010

- Keynote address on "Ethics in Profession" at St.Joseph's Engg. college Mangalore in April 2010
- Lecture on "Physical and MAC Layer issues in Sensor Networks" at AICTE sponsored, IT Department organized, summer school at NITK, Surathkal on 25 July 2008
- Lecture on "Embedded and Real time Systems" at IEEE Students' chapter of Canara College of Engg., Mangalore on 5 April 2008
- Technical Talk on "Processor IC Design from a Computer engineer's Perspective" at VCET, Puttur on 15 March 2008
- Lectures on "Secure Data Exchange and Data Formats" and "Server Sizing and Benchmarking" to the IT officers of Corporation Bank at Bank Head Office, Mangalore , from 21 - 22 October 2007
- "E-learning", during UGC sponsored State Level Workshop for teachers at Mahavira College,Moodabidri, on 24 March 2007
- " Computer Science as a Career", at BhuvanendraCollege, Karkala, Jan 2006
- "VLSI Synthesis", at MalnadCollege of Engineering Hassan, on 16 February 2006
- "VLSI CAD and Low power VLSI issues", at BVB college of Engineering , Hubli, on 10 December 2005
- "Embedded System & RTOS", at VivekanandaCollege of Engineering, Puttur, on 24 November 2005
- ""Embedded Systems and Computing", at Jawaharalal Nehru College of Engineering, Shimoga on 8 November 2005
- "Preparing for ISO Certification and NBA Accreditation", at KVG college of Engineering, Sullia, August 2005
- "Procurement procedure under TEQIP", at Sir MVIT, Bangalore, June. 2005
- "IC Technologies", at St. .Joseph's College of Engineering, Mangalore on 31 October 2004
- "Wi-Fi Technology", during Telecommunication Day Celebrations, at DK District Telecom Office, Mangalore, 2004

- "Security issues in Data Communication" at Institute of Engineers, Local Chapter Office, Mangalore, 2004
- "Technical paper Writing", at ISTE Chapter, NMAMIT, Nitte, 2004
- "Research Areas in Data Mining", at MIT, Manipal, on 3 August 2003
- "Compiler Design" at Dept. of MCA, NITK, Surathkal during January 2002
- Lecture series on "Computer Architecture " at PDA College of Engineering, Gulbarga during 1992
- Lecture Series on "Computer Organization" at Kalpataru Institute of Technology, Tiptur during 1991.

15.2   Contribution to CSE Departmental Activities

- Management responsibilities as Head of the Department of CSE
- Examiner for V.T.University Belagavi, Mangalore, Bangalore, Kuvempu, Kannur and Goa Universities

15.3   Contribution to College Activities

- ISO Management Representative (MR) of NMAMIT, Nitte from 2003 to 2010
- TEQIP (Technical Education Quality Improvement Programme) Nodal officer from 2006

15.4   Online Webinars:

- Participated as a speaker in the webinar on "Challenges and Concerns of Engineering Education post COVID" arranged by IIK(Indians In Kuwait) on 4 July 2020
- Participated as a speaker in the webinar on "Engineering Education and Employment Prospects- Post COVID" organized by NMAMIT, Nitte on 20 June 2020
- Participated as a panelist in the "Fronius India Academician conclave 2020"on 19 June 2020, along with Mr. V.V.Kamath, MD of Fronius India, Dr.R.K.Agarwal from A.K.Garg Engg college, Sri Sanjib Kumar Rout from C.V. Raman Global University, Dr.R.D.Patidar from O.P.Jindal University & Dr.Murugaiyan Amrithalingam from IIT Chennai

16. Publications

16.1 International

1. P.Srinivasa Pai, **Niranjan N.Chiplunkar,** Shashikantha Karinka and B.R. Shrinivasa Rao, "The Research-teaching Nexus in an Autonomous Institution: A Case Study of Post Graduate Students' Awareness, Experience and Perceptions of Research", ISSN (Print):2231-6043, ISSN(Online):2395-1907, Vol 12 (1&2), Page 20-29, January-December 2018.

2. Anisha P Rodrigues, Niranjan N Chiplunkar, Roshan Fernandes "Aspect-based classification of product reviews using Hadoop framework", Cogent Engineering (ISSN: 2331-1916), Volume 7, 2020- Issue 1, Taylor and Francis Publication, Journal Metrics: **Scopus**, **Web of Science Indexed(Q2), SJR:0.323, SNIP:0.911, cite score:2.5,** Link: https://doi.org/10.1080/23311916.2020.1810862

3. "Artificial Intelligence-Based Chatbot Framework with Authentication, Authorization and Payment Features" by Deena Deepika Cutinha, Niranjan N.Chiplunkar, Shazad Maved and Arun Bhat, Springer Nature Singapore Pte Ltd.2021, Page 179 to 187.

4. "Investigation of Security Issues in Distributed System Monitoring" by Manjunath Kotari, Niranjan N.Chiplunkar in Handbook of Computer Network and Cyber Security, page 609-634, publisher: Springer, Cham, published in 2020.

4. "A survey on Detection and Analysis of Cyber Security Threats through Monitoring Tools" by Manjunath Kotari, Niranjan N.Chiplunkar, in Handbook on Research on Intrusion Detection Systems, page: 77-104, publisher: IGI Global, , published in 2020.

5. "Social Big Data Mining : A survey focussed on Sentiment Analysis". by Anisha P.Rodrigues, Niranjan N.Chiplunkar ,Roshan Fernandes in Handbook of Research on Emerging Trends and Applications of Machine learning, page 528-549, publisher IGI Global, published in 2020.

6. "Evaluation of Video Watermarking algorithms on mobile device" by P.S.Venugopal, H.Sarojadevi, Niranjan N Chiplunkar,International Journal of Computer Aided Engineering and Technology, Volume 12, issue 1, page 55-67,Inderscience publishers (IEL), published in 2020.

7. Raju K, **Niranjan N Chiplunkar,** "A Performance Improvement Approach for CPU-GPU Heterogeneous Computing", International Journal of Recent Technology and Engineering (IJRTE)ISSN:2277-3878, Volume-8, Issue-1, May 2019. Page:532-538

8. Anisha P Rodrigues, **Dr. Niranjan N Chiplunkar**, "A new big data approach for topic classification and sentiment analysis of Twitter data", Evolutionary Intelligence (ISSN: 1864-5909), Springer Publication, Journal Metrics: IPP: 1.30, SJR: 0.280, SNIP: 1.064, Scopus Indexed, DOI: https://doi.org/10.1007/s12065-019-00236-3 pp 1-11.

9.Anisha P Rodrigues, Dr.Niranjan N Chiplunkar, "Aspect Based Sentiment Analysis on Product Reviews", IEEE Sponsored Fourteenth International Conference on Information Processing (ICInPro-2018), Bengaluru. Date Added to IEEE Xplore: 20 May 2020, DOI: 10.1109/ICINPRO43533.2018.9096796.

10. Pai B. H. Karthik , H. R. Nagesh, **Niranjan N. Chiplunkar**, Mitigation and performance evaluation Mechanism for Selfish Node Attack in MANETs, DOI: 10.1109/ICCUBEA.2017.8463847, Electronic ISBN: 978-1-5386-4008-1, Print on Demand(PoD) ISBN: 978-1-5386-4009-8, September 2018, IEEE.

11. Anisha P Rodrigues, Dr. Niranjan N Chiplunkar "Real time Twitter data Analysis using Hadoop Ecosystem ", Cogent Engineering (ISSN: 2331-1916), Volume 5, 2018 - Issue 1, Taylor and Francis Publication, Journal Metrics: IPP:0.93, SJR:0.204, SNIP:0.551, **Scopus,Web of Science Indexed.** DOI: https://doi.org/10.1080/23311916.2018.1534519, *5*(1), p.1534519.

12. Prasanna Kumar H R, **Niranjan N Chiplunkar**, Visual Cryptographic Mechanism for Secret Information Sharing, International Journal of Engineering & Technology, 7 (3.34) (August-2018) 775-780. SNIP-0.000, IPP-0.000, SJR-0.000.

13. Prashant G Desai , Sarojadevi H, **Niranjan N Chiplunkar**, Interactive Intelligent Software System and NLP Techniques for Document Processing, International Journal of Engineering and Technology (UAE), SCOPUS index, DOI: 10.14419/ijet.v7i3.34.19559, Vol.7 (3.34) Special issue 34, pp 775-780, August, 2018

14. Sumathi, **Dr. Niranjan N Chiplunkar**, "Populating Parameters of Web Services by Automatic Composition Using Search Precision and WSDL Weight Matrix", IJCSE, 1742-7193, InderScience Publishers – Scopus indexed, October 2018.

15. Raju K, **Niranjan N. Chiplunkar**, "A survey on techniques for cooperative CPU-GPU computing", International Journal Sustainable Computing: Informatics and Systems, Volume 19, September 2018, Pages 72-85. SNIP- 0.911, IPP-1.60, SJR- 0.362.

16. Srinivas Pai, **Niranjan Chiplunkar**, Srinivas Rao "Project based Learning: issues faced by faculty for its effective implementation, **5th International Conference** on Transformation in Engineering Education (ICTIEE), from 8- 9 January 2018, Thiagarajar College of Engineering, Madhurai

17. Karthik Pai.B.H. Dr. Nagesh H R, **Dr. Niranjan N Chiplunkar**, "Performance analysis of AODV and OLSR against routing attacks on random topologies in MANETS", "**International Journal** of communication networks and distributed systems"-(under review)

18. P. Srinivasa Pai and **Niranjan N.Chiplunkar**, "Research Based Curriculum to Improve the Teaching-Learning Experience of Undergraduate Students", Vol 11(2), DlI:10.18311 ISSN 2231-6043, Page 71-76/December 2017.

19. Bhavitha BK,Anisha P Rodrigues, **Dr. Niranjan N Chiplunkar** "Comparative study of machine learning techniques in Sentimental analysis",**International Conference** on Inventive Communication and Comoutational Technologies(ICICCT-2017),held during 10-11,March 2017,at the Ranganathan Engineering College,Coimbatore,India,to be published in IEEE Explore.Date Added to IEEE Xplore:17 July 2017, DOI:10.1109?ICICCT.2017.7975191.

20. Mr. Ramakrishna B., Asst. Professor & Head, Dept. of Humanities, NMAMIT, Nitte and **Prof. Niranjan N.Chiplunkar,** Principal, NMAM Institute of Technology, Nitte"A Holistic Cumulative Approach for a Sustainable Transformation of Engineering Education", International Journal of Engineering Technology Science and Research, IJETSR, ISSN 2394-3386, Volume 4, Issue 11, November 2017.

21. Anisha P Rodrigues, **Dr. Niranjan N Chiplunkar** "Big data Mining :A survey on techniques involved in sentimental analysis", 13th International IEEE India conference INDIACOM-2017 organized by BVICAM,New Delhi from 01-03 March 2017.

22. Karthik Pai B.H. Dr. Nagesh H R, **Dr. Niranjan N Chiplunkar**, "Mitigation Mechanism for Selfish node Attack in MANETs", "International Conference on Computing, Communication, Control and Automation, Pimpri Chinchwad College of Engineering, Pune", 2017 (IEEE Xplore/Scopus indexed)

23. Venugopala P. S, Sarojadevi H, **Niranjan. N. Chiplunkar**, Evaluation of Video Watermarking Algorithms on Mobile Device, International Journal of Computer Aided Engineering and Technology in June 2017. **Inderscience publication**, ISSN online: 1757-2665 ISSN print: 1757-2657. Accepted for publication

24. Sunitha N V, Raju K, and **Niranjan N Chiplunkar**, "Performance Improvement of CUDA Applications by Reducing CPU-GPU Data Transfer Overhead", ICICCT-2017, REC, Coimbatore., IEEE/ ISBN: 978-1-5090-5297-4/17 ©2017 IEEE.

25. Sumathi, **Dr. Niranjan N Chiplunkar**, "Dynamic Composition of Web Services by Service Invocation Dynamically" , IJEME ISSN: 2305-3623(Print), ISSN: 2305-8463, July 2017, DOI: 10.5815/ijeme.2017.04.05, pp 41-50 (open access).

26. Sumathi, **Dr. Niranjan N Chiplunkar** "Open Source APIs for Processing the XML Result of Web Services", International Conference on Advances in Computing, Communications and Informatics (ICACCI) Paper-ID—2017 Paper ID : 1570363306 (Scopus indexed & Thomson Reuters indexed conference). Paper Category: Regular Paper (Oral Presentation), MIT – Manipal, IEEE-2017.

27. Anisha P Rodrigues, Archana Rao, **Dr. Niranjan N Chiplunkar**, "Sentiment Analysis of Real Time Twitter data using Big data Approach",IEEE Sponsored 2nd International Conference on Computational Systems and Information Technology for Sustainable Solution (CSITSS-2017),Date added to IEEE Xplore : 30 August 2018, DOI:10.11.09/CSITSS.2017.8447656

28. Aswitha TA,Anisha P Rodrigues,**Dr. Niranjan N Chiplunkar**, "Movie Dataset Analysis using Hadoop-Hive",IEEE Sponsored 2nd International Conference on Computational Systems and Information Technology for Sustainable Solution (CSITSS-2017), Date Added to IEEE Xplore:30 August 2018, DOI:10.11.09/CSITSS.2017.8447828

29. Anisha P Rodrigues,**Dr. Niranjan N Chiplunkar**,Aswitha TA "Big Data

Analytics with Hadoop-Hive and MySQL:A Case Study",4th International Conference Computational Methods in Engineering and Health Sciences(ICCMEH-2017), held during 19-20,December 2017, at MIT Manipal,India.

30. Venugopala P. S, Dr. H.Sarojadevi, **Dr. Niranjan.N. Chiplunkar**, An Approach to Embed Image in Video as Watermark Using a Mobile Device, **Elsevier Journal of Sustainable Computing Informatics and Systems.** ISSN: 2210-5379. Submitted on 3 February 2017, first revision submitted on 6 April 2017, published on 17 June 2016. (https://doi.org/10.1016/j.suscom.2017.06.003)

31. "Strengthen the Security of Confidential Information Using Cryptographic Techniques", Prasanna Kumar H.R, Dr. **Niranjan N Chiplunkar,** International Journal of Applied Engineering Research, Volume 12, Number 16, pp. 5631-5636, 2017

32. "Cryptographic Mechanism to Transfer Secret Information", Prasanna Kumar H.R, **Niranjan N Chiplunkar**, International Journal of Electrical Engineering and Informatics (IJEEI): Communicated on 6 April 2017 (Paper is under review process)

33. R. Balasubramani, **N N Chiplunkar,** "Attainment of Programme Outcomes Through Course Outcomes in Outcome Based Educaiton: A Case Study" Journal of Engineering Education Transformations, Volume-31, Issue - (2) pp 26-30, 1-10-2017

34. Srinivas Pai, **Niranjan Chiplunkar**, Srinivas Rao and Subrahmanya Bhat "Effective use of information and communication technology in enhancing teaching-learning outcomes: Some thoughts" Proceedings of 3rd **International Conference** on transformation in engineering education, ICTIEE 2016, COEP, Pune on 8 and 12 January 2016 and also published in Journal of Engineering Education Transformations (JEET), 2015, ISSN 2349-2473, eISSN 2394-1707 Vol 30, No.1, July 2016, pp 30-38.

35. Anisha P Rodrigues**, Niranjan N. Chiplunkar**, Anujna Rao "Sentiment Analysis of Social Media Data using Big Data Processing Techniques" published in IJCA(International Journal of Computer Applications) ",Volume 22 – Issue-No.6, October 2016,pp-56.

36. Venugopala P. S, Ankitha.A. Nayak, Dr. H.Sarojadevi, **Dr.Niranjan.N.** Chiplunkar., Video Watermarking for Android Mobile Device, Proceedings of the 10th INDIACom; INDIACom-2016; IEEE Conference ID: 37465 2016 3rd International

Conference on "Computing for Sustainable Global Development", from 16 - 18 March, 2016, Bharati Vidyapeeth's Institute of Computer Applications and Management (BVICAM), New Delhi (INDIA) pp 3554 – 3560

37. Venugopala P. S, Shravya Jain, Dr. H.Sarojadevi, **Dr.Niranjan.N. Chiplunkar**., Study of Possible Attacks on Image and Video Watermark, Proceedings of the 10th INDIACom; INDIACom-2016, 3rd 2016 International Conference on "Computing for Sustainable Global Development", from 16 – 18 March, 2016, Bharati Vidyapeeth's Institute of Computer Applications and Management (BVICAM), New Delhi (INDIA) pp 3505 - 3510

38. Venugopala P. S, Dr. H.Sarojadevi, **Dr.Niranjan.N. Chiplunkar**, Savitha Kamath, An Approach for Bitstream Video Watermarking on Mobile Device, IEEE International conference on Signal processing, Communication power and Embedded system (SCOPES-2016), held at Centurion University, Odissa, India from 3 – 5 October 2016

39. Anisha P Rodrigues, **Dr. Niranjan N Chiplunkar** "Mining Online Product Reviews and Extracting Product features using Unsupervised method", 13th International IEEE India conference INDICON-2016 organized by IEEE india chapter at IISc Bangalore during 16-18 December 2016, Date added to IEEE Xplore:02 February 2017, DOI: 10.1109/INDICON.2016.7839054.

40. Durga Prasad, **Niranjan N.Chiplunkar,** K. Prabhakar Nayak, *"Performance Analysis of the Physical and Medium Access Control Layer Parameters with Effect of Varying Transmission Power using IEEE 802.15.4 Standard for Wireless Body Sensor Network",* 6th International Conference On Advances In Computing & Communications, ICACC 2016, held on 6-8 September 2016, at Cochin, India, available at Elsevier's Science Direct Procedia of Computer Science, pp. 639 – 646

41. Prashant G Desai, Sarojadevi, , **Niranjan N Chiplunkar** "A Study of Natural Language Processing Based Algorithms for Text Summarization", Proceedings of International Journal of Innovative Research in Science, Engineering and Technology, ISSN : 2347-6710, Vol.5, Issue-9, May 2016, pp 304-308

42. Sanju V, **Niranjan Chiplunkar,** "RiCoBiT - Ring Connected Binary Tree : A Structured And Scalable Architecture For Network On Chip Based Systems : An Exclusive Summary", International Conference on Data Engineering and

Communication Technology-ICDECT, Pune, March 10 – 11 2016 (Indexed In Springer)

43. Dr. Sarojadevi H., Dr.D.K.Sreekantha, **Dr.Niranjan N.Chiplunkar** "A Study of Computing Techniques in Agricultural and Food Sector" **International Journal** of Engineering Research & Technology (IJERT) ISSN:2278-0181, ICESMART-2015 Conference Proceedings, Volume 3, Issue 19, Published by www.ijert.org.

44. Dr.Sanju, **Dr.Niranjan Chiplunkar** "RiCoBiT :Ring Connected Binary Tree:A structured and Scalable architecture for Network on Chip Based System", 2015 **International Conference** on computing and network communication (CoCoNet 15), Dec. 16-19 2015, Trivandrum India, 978-1-4673-7308-1/15 IEEE 2015

45. Dr.Sanju, **Dr.Niranjan Chiplunkar** "Design and implementation of Mesh and torus for network on chip based systems", Proceedings of **International Conference** on Trends in Automation , Communication and Computing Technologies (I-TACT 15), 21st and 22nd December 2015, Acharya Institute of Technology, Bangalore. 978-1-4673-6666-3/15 IEEE 2015

46. **N. Chiplunkar**, V.Krishna, " SMITHA:Scalable Modular Interconnect for Three Dimensional High Performance Applications-! New 3 D Topology for NoC Based Systems", BVICAM's International Journal of Information Technology, Vol-7, Issue-1, Jan 2015.

47. Ankitha.A.Nayak, Venugopala P. S, Dr. H.Sarojadevi, **Dr.Niranjan.N. Chiplunkar.,**An Approach to Improvise Canny Edge Detection using Morphological Filters, International Journal of Computer Applications (0975 – 8887) Volume 116 – No. 9, April 2015 (pp 38-42) DOI:10.5120/20368-2575

48. Venugopala P. S, Ankitha.A.Nayak, Dr. H.Sarojadevi, **Dr.Niranjan.N. Chiplunkar**., Various challenges in Video Watermarking for Android Mobile Devices, IEEE International Conference on Information Processing, 16th to 19th December 2015, Vishwakarma Institute of Technology, Pune. DOI: 10.1109/INFOP.2015.7489388.

49. Sanju V., **Niranjan Chiplunkar**, Suresh Mandia, Nancy Jain, Nikita Jaiswal, M.Khalid "Design and Implementation of RiCoBiT: A structured and Scalable Architecture for Network on Chip Based Systems" **International Journal** of computing and digital Systems, ISSN 2210-142X , Vol 4 No. 1 Jan 2015, http://dx.doi.org/10.12785/ijcds/040107

50. Sumathi, **Niranjan N Chiplunkar** "Necessity of Dynamic composition plan for Web Services", Paper presented at **International Conference** on Applied and Theoretical Computing and Communication Technology IEEE-2015.

51. Sumathi, Dr. **Niranjan N Chiplunkar** "Dynamic search and selection of Web Services"**, International Conference** on Applied and Theoretical Computing and Communication Technology, TamilNadu, India IEEE-2015.

52. Srinivas Pai, **Niranjan Chiplunkar**, B.R.Srinivas Rao, K.Subramanya Bhat " Undergraduate students' research: A case study of their experience and perceptions in an autonomous Institution" eISSN: 2394-1707 , Proceedings of 2nd**International Conference** on Transformation in Engineering Education(ICTIEE 2015), 5th and 8th January 2015, BMS College of Engineering Bangalore. PP 34-39, Journal of Engineering Education Transformations.

53. Prashant G Desai, Sarojadevi, **Niranjan N Chiplunkar** "A Template Based Algorithm For Automatic Summarization And Dialogue Management For Text Documents", Proceedings of International Journal of Research in Engineering and Technology, ISSN : 2321-7308, Vol.4, Issue-11, 2015,pp 334-340

54. Mahesh Kini M, Prashant G Desai, Sarojadevi, , **Niranjan N Chiplunkar** "Text Mining Approach to Classify Technical Research Documents using Naïve Bayes", Proceedings of International Journal of Advanced Research in Computer and Communication Engineering, ISSN : 2278-1021, Vol.4, Issue-7, 2015, pp 386-391

55. Prashant G Desai, Sarojadevi, , **Niranjan N Chiplunkar** "Rule-Knowledge Based Algorithm for Event Extraction", Proceedings of International Journal of Advanced Research in Computer and Communication Engineering, ISSN : 2319-5940, Vol.4, Issue-1, 2015, pp 79-85

56. Sanju V, Suresh Mandia, Nancy Jain, Nikita Jaiswal, **Niranjan Chiplunkar**, M Khalid, "DesignAnd Implementation of RiCoBiT: A Structured and Scalable Architecture For Network On Chip Based Systems", International Journal of Computing and Digital Systems, Vol No 4, Issue No 1, Jan 2015, Pg No 63 – 71

57. Sanju V,**Niranjan Chiplunkar**, Venkata Krishna, "SMITHA: Scalable Modular Interconnect for Three dimensional High Performance Applications - A New 3D Topology for NoC Based Systems" International Journal Of Information Technology,

Vol 7 Issue 13 No1, Jan2015 ISSN 0973-5658 (Impact Factor: .605), pp-803.

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162. Ashok Kumar **&Niranjan Chiplunkar**, "Auction Based Detailed Router for 3D FPGA", PGFEST'05, NMAMIT, Nitte, April 2005

163. **Niranjan N. Chiplunkar**, "Distributed computing and JAVA", Proc. Of National Conference on Distributed Computing, NCDC-2004, NMAMIT, Nitte. 183-187pp.

164. Ganapathi Hegde and **Niranjan N. Chiplunkar**, "Application and Management of reconfigurable computing using operating systems", Proc. Of National Conference on Distributed Computing, NCDC-2004, NMAMIT, Nitte. 168-173pp.

165. Ashok Kumar, **Niranjan N. Chiplunkar**, and Ganapathi Hegde, "Distributed Computing in VLSI", Proc. Of National Conference on "Control, Communication and Information Systems: Volume II" from 23-24 January 2004, Farmagudi, Goa. 20-24pp.

166. Ganapathi Hegde and **Niranjan N. Chiplunkar**, "High level partitioning with synthesis for Multi-FPGA systems", Proc. Of National Conference on "Control, Communication and Information Systems: Volume II" from 23-24 January 2004, Farmagudi, Goa. 9-13pp.

167. **Niranjan N. Chiplunkar**, "RMI and CORBA –a framework for Distributed Computing application Development", Proc. Of National Conference on "Control, Communication and Information Systems: Volume II" from 23-24 January 2004, Farmagudi, Goa.1-5pp.

168. Nagesh H.R., K.Chandrashekharan, and **Niranjan N. Chiplunkar**, "Design Implementation and analysis of Group communication using key graphs", Proc. Of National conf. on "Recent Trends in Network Technology", from 13-15 December 2003, Karpagam Engg. College, Coimbatore.

169. Ashok Kumar **&Niranjan Chiplunkar,** "Low Power FPGAs", PGFEST'03, NMAMIT, Nitte, from 25-26 July 2003

170. Ganapathi Hegde **&Niranjan N. Chiplunkar**, "Multi FPGA system and its applications", Proc. Of PGFEST 2003, from 25-26 July 2003, NMAMIT, Nitte. 35pp.

171. **Niranjan N. Chiplunkar**, "E-Learning:Use of IT in Teaching Learning Process", Proceedings of 32nd National Convention of ISTE(Indian Society for Technical Education), from 27-29 December 2002. 62-63pp.

172. **Niranjan N. Chiplunkar**, "Review of Spatial DataBases", Proc. Of National Conference on Recent Trends in DBMS, concepts & Practice, Nitte,India, March 2002. 209-216pp.

173. **Niranjan N. Chiplunkar**, Manoj V.N. & Manjaiya, "Design of High speed RSA cryptor Chip", Abstract. Of National Conf. On Signals, systems and Security, PSG Tech., Coimbatore,India, March 2002. 32pp.

174. **Niranjan N. Chiplunkar**, Manoj V.N. & Manjaiya, "Application of Chinese Remainder Theorem in designing a High sped RSA crypto chip", Proc. Of 2nd National Conference on Advanced Computing, PSG Tech, Coimbatore,India, Feb. 2002. 37-46pp.

175. **Niranjan N. Chiplunkar** & Manoj V.N, "Computational Aspects of certain Placement and Routing algorithms of VLSI system", Proc. Of 2nd National Conference on Advanced Computing, PSG Tech, Coimbatore, India, Feb. 2002. 51-55pp.

176. **Niranjan N. Chiplunkar** & Chitrasena Bhat, "Algorithm for Area Optimization in LookupTable based FPGA Technology mapping", Proc. Of annual CSI convention, NewDelhi, Sept. 1998. 149-155pp.

177. M P Tahiliani, **N N Chiplunkar,** " A Review of Spintronics based Data Storage" Computer Journal, Volume 2, Pages 12, 1998.

178. **Niranjan N. Chiplunkar**, "Technology mapping in FPGAs", Proc. Of National Conference on IT for 21st Century, Surathkal, India, 19-21st Dec.1996. 32-38pp.

179. **Niranjan N. Chiplunkar**, "Layout Compaction of VLSI circuits –an overview", MIT Technical Review journal, Manipal, vol. 4 Dec 1996. 58-64pp.

180. **Niranjan N. Chiplunkar**, "CAD for VLSI design", CSI Communications Magazine , June 1993. 11-14pp.

181. **Niranjan N. Chiplunkar**, "CAD for VLSI", The IEEE Annual, Journal, MIT, Manipal, May 1992. 25-27pp.

17. Books& Proceedings

- Edited Book "Advances in Artificial Intelligence and Data Engineering" Select Proceedings of AIDE 2019, Volume 1, Niranjan N.Chiplunkar, Takanori Fukao,

Springer.com (ISBN:978-981-15-3513-0) (ISSN 2194-5357) Advances in Intelligent Systems and Computing 1133.

- Book Published "Introduction to Parallel Computing" Dr. Niranjan N.Chiplunkar, Mr. Raju K, during January 2020 (ISBN: 978-81-265-5681-6) Wiley India Pvt. Ltd.

- A self published book on "*A Novel Architecture Design for Network on Chip*" jointly authorized by Dr. Niranjan N.Chiplunkar, and Dr. Sanju in **May 2020**.(ISBN 978-93-5406-678-8)

- "*VLSI CAD*" Text book – Niranjan N.Chiplunkar & Manjunath Kothari, PHI Learning New Delhi, **May 2011** (ISBN: 978-81-203-4286-6)

- "*CAD for VLSI*" Text Book**- Niranjan N.Chiplunkar**&Manjunath Kothari**, NMAMIT Publication**, **February, 2007** (ISBN : 978-81-7525-820-4)

- Member of Editorial Board of NMAMIT Annual Research Journal (ISSN: 2249-0426)

- Edited "*Proceedings of 32nd National Convention of Indian Society for Technical Education*" , B.S.Samaga, **Niranjan N. Chiplunkar,** December 2002

- Edited "Proceedings of International conference & Symposium on Storage Technologies (ICSST)" **Niranjan Chiplunkar**, Mr.Manjunath Kothari, 2008 (ISBN: 978-81-907846-0-3)

- Edited "Proceedings of International conference on Computer Architecture, Networking and Applications (ICCANA)" **Niranjan Chiplunkar**, Dr.Uday Kumar Shenoy , 2011(ISBN: 93-80697-42-2), published by Excel India Publishers, New Delhi

18. Research Grants Obtained:
- Intel-FICE academic fund of **Rs.2.00lakh** for setting up "Embedded Application lab. Using Intel ATOM processor" November 2011 . Also 10 Intel ATOM kits have been received costing about Rs. 2 lakhs.

- DST Project titled "Software architecture and programming model to enhance the performance of application on multi-core platform" (DST/TDD/03) for 2 years from 1 January 2010 to 31 December 2011. Amount sanctioned :**Rs.16.00 lakhs**(As Principal Investigator)

- AICTE-RPS(B) project titled "Study and design of algorithms for VLSI CAD tool development towards applications involving mutually exclusively modules" for 2 years from 1 December 2008 to 30 November 2010, Amount sanctioned: **Rs.4.6lakhs** (As Co-Principal Investigator)

19. Research Interests

Algorithm design for VLSI CAD, Multi core architecture and Program Optimization, Ad-hoc Networks, E-learning, Advanced Microprocessors

20. Subjects Taught

Logic Design, Design and Analysis of Algorithms, Finite Automata & formal Languages, Computer Organization & Architecture, Systems Programming, RDBMS, Microprocessors & Peripherals, Principles of Programming Languages, compiler Design, Computer Graphics, VLSI Design, C# Programming & .NET Concepts, Embedded and Real Time Systems, Cryptography and Network Security, MIS, CAD for VLSI, System simulation and Modelling

21. Ph.Ds guided (12)

- Dr.Ashok Kumar - Ph.D.completed- VTU, Belagavi – 2010
- Dr.Yuvaraju – Ph.D. completed – VTU, Belagavi-2012
- Dr.Sanju, -Ph.D. completed , VIT, Vellore- 2015
- Dr.Prasanna Kumar – Ph.D. Completed- VTU - Guide 2017
- Dr.Manjunath Kothari – Ph.D. Completed – VTU -Guide 2017
- Dr. Durga Prasad – Ph.D. Completed– Manipal University-Guide 2020
- Dr. Sumathi Pawar – Ph.D. Completed –VTU -Guide 2017
- Dr. Anisha Rodrigues – Ph.D. completed –VTU –Guide 2020
- Mr.Raju-Ph.D. completed – VTU -Guide-2022
- Dr.Karthik Pai – Ph.D. Completed – VTU-Co-guide, 2017
- Dr.Venugopal P S –Ph.D. Completed –VTU-Co-guide, 2018
- Dr.Prashanth Desai –Ph.D. Completed –VTU-Co-guide, 2017

22. Miscellaneous

- Developed "Computer Graphics Laboratory" at Manipal Institute Technology, Manipal, India in March 1988

- Reviewer for the Proceedings of IEEE Conference on "VLSI Design" since 2002.

- Chair of Programme Committee, National Conference on "Distributed Computing", NMAMIT, Nitte India, 2004

- Coordinator for *FIVE* ISTE sponsored Short Term Training Programs

- Set up "Data Mining Laboratory" at NMAM Institute of Technology, Nitte during Jan. 2006

- Chair of Organizing Committee, National Conference on "Ubiquitous & Pervasive Computing", NMAMIT, Nitte, India from 24-25 January 2007

- So far, guided 42 BE level projects and 17 M.Tech. level projects

- President of Rotary Club of Nitte during 2006-07

- Consultant for a Patent Search organization :EntellectLLP.com

- **Applied for an Indian Patent**, based on research work (application no.: 428/CHE/2008)

- **Applied for US Provisional Patent** in April 2010 for the research work on "New Architecture for Network on chip (NoC) systems" (Application Number:61/395,480 dated 14/5/2010)

- **Patent published application number 201941009494, Automatic Footwear Stand during 12 March 2019.**

- **Patent granted for application number 1598/CHE/2014, SMITHA, Scalable Modular Interconnect For Three Dimensional High Performance Application -A New 3D Topology for NOC Based Systems during 2020.**

- **Applied Patent Wheel Chair with double scissor jack mechanism for height adjustment for Creeping Patients**

- Received AICTE travel grant for attending the conference at Shanghai, China

- Was Chairman of BOE (Board of Examination)–Computer Science at MangaloreUniversity during 1997-98. Was member of BOE-Computer Science of Visvesvaraya Technological University ,Belgaum during 2004-05, was member of BoS of BVB College of Engineering, Hubli and MIT, Manipal
- BOS and BOE Chairman of NMAMIT Nitte for Computer Science UG and PG programs under Autonomous Scheme.
- Board of Studies member of Computer Science at MIT, Manipal University, NMIT Bangalore, Member of BoS (ISE) at Nagarjuna College of Engineering & Technology, Bengaluru from (21 to 31 July 2017). SDMCE Dharwad(2008), BVBCE Hubli(2008), Bangalore, Dr.AIT Bangalore and St.Aloysius College Mangalore (2-6-2007 for two years).
- Governing Council Member of T.John Engg. College Bangalore(from 01 – 31 March 2016), Cauvery Institute of Technology, Mandya(15 December 2017 to 30 November 2019) Academic Council Member of Dr.Ambedkar Institute of Technology, Bangalore (from 1 March 2013 to 28 February 2015), NMIT,Bangalore, Member of Nitte University (from 4-12-2013 to March 2015).
- President of Hockey India Udupi from 2014 July
- Program Chair of 2016 IEEE Distributed Computing, VLSI, Electrical Circuits and Robotics (DISCOVER) Conference, from 13-14 August 2016, NITK, Surathkal
- Member of Governing Council of KLE College of Engg. & Technology, Chikkodi and member of BoS in Computer Science and MCA at NIE, Mysore
- Founding Director of a Startup 'Inspirante Technologies Pvt. Ltd' started in the year 2019.

23. Seminars/conferences /Continuing education programs Attended:

International:

- EMC World conference at Las Vegas USA 19-22 May 2008
- Conference on Computer Networking, Bangkok, Thailand, 24 April 2010

- Conference on Computer Research and Development , Shanghai, China , from 10-12 March 2011

National

- Ten days Faculty development program at IISc on "Computer Graphics and CAD" during April 1988
- Three days training program on "Software Engineering" at Infosys Bangalore during August 1992. Resource Person :Prof. H.N.Mahabala
- Five days STTP on "Advanced JAVA at PSG, coimbatore during 2001
- Five days STTP on "COM & DCOM" at NITK, Surathkal during November 2005
- Three days TEQIP Workshop on Financial Management at Indian Institute of Financial Management at Faridabad during November 2006
- Five days IUCEE workshop on "Advanced compilation Technicque" at Infosys campus Mysore during June 2008. Resource person: Prof. Jack Davidson, from Virgina Tech., USA
- Five days IUCEE workshop on "Advanced Microprocessors" at Infosys campus, Mysore during June 2009. Resource person : Prof. Rafiquzmann, California Polytechnic at Pamola
- Many more one day/two day programs at NMAMIT Nitte, MIT Manipal , NITK surathkal, MSRIT Bangalore and other colleges
- One day Faculty development program on "Predictive Analytics" at Accenture Services Bangalore on 16 December 2015

24. **Seminars/Conferences/continuing education programs Organized:**

- AICTE-STTP on "Theory & Practice of CAD for VLSI" – from 2 -14 September 2002
- AICTE-ISTE Winter school on "Object Oriented and visual programming" from 17-29 January 2000

- AICTE-ISTE STTP on "E-learning, XML and Multimedia"- from 9-21 February 2004

- National Conference on "Mobile and Pervasive computing", from 16-17 September 2009

- 22nd CSI Karnataka Student Convention from 7- 8 November 2008

- National Conference on Ubiquitous computing, in 2006

- National level Workshop on "Device Drivers on Multiplatforms" in 2007

- International Conference on "Storage Technologies" from 3-4 December 2008

- International conference on "Computer Architecture, Networking and Applications", from 7-8 January 2011

- International Conference on "Emerging Trends in Engineering", May 2012

- Many more one day/two day programs at NMAMIT, Nitte for the teaching/non teaching staff of NMAMIT and other colleges.