

## Bio-Data

1. **Name** : NIRANJAN N. CHIPLUNKAR  
2. **Qualification** : B.E, M.Tech, Ph.D  
3. **Sex** : Male  
4. **Marital Status** : Married  
5. **Date of Birth** : 27<sup>th</sup> Dec, 1964  
6. **Category** : General  
7. **Nationality** : Indian  
8. **Present Position** : Principal,  
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9. **Address for communication** :

Principal

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**10. Academic Record:**

- Graduation** : B.E in E&C from NIE, Mysore, India, 1986 -(FCD)  
**Post-Graduation** : M.Tech in CSE from MIT, Manipal, India, 1990-(FCD)  
**Doctorate** : Ph.D in CSE from University of Mysore, India, 2002  
**Area of Research** : CAD for VLSI  
**Topic** : Design of Algorithms for optimum area utilization in VLSI ASICS

**11. Experience:**

<b>From</b>	<b>To</b>	<b>Organization</b>	<b>Designation</b>	<b>Total Period</b>
1987	1993	Manipal Institute of Technology (MIT), Madhav Nagar Manipal, Karnataka, 576104	Lecturer	6 Years
1993	1996	Manipal Institute of Technology (MIT), Madhav Nagar Manipal, Karnataka, 576104	Reader	3 Years
1997	2003	NMAMIT, P.O: Nitte Karkala Taluk, Udupi District, Karnataka, India-574 110	Professor	7 years
2003	2012	NMAMIT, P.O: Nitte Karkala Taluk, Udupi District, Karnataka, India-574 110	Professor & Head of the Department	9 years
2010	2012		Dean(Academics)	
13 <sup>th</sup> June 2012	Till date	Principal, NMAIT, Nitte		
<b>Total</b>				<b>30 years</b>

## **12. Membership in Professional Bodies**

- Life Member of ISTE (LM816 )
- Senior Member of IEEE (01492834)
- Senior Member of CSI (F8000642)
- Fellow of IE(India)

## **13. Awards and Recognitions**

- “ Bharatiya Vidya Bhavan National award for Best Engineering College Principal 2014” given by ISTE
- “Excellent Achievement Award” by Centre for International Cooperation on Computerization (CICC), Govt. of Japan in 2002
- “Outstanding Alumnus of MIT, Manipal”, by Manipal University, Manipal, India in 2008
- Resume is published in “Marquis Who’s Who in World” in January 2009

## **14. Overseas Experience**

- Attended a training on “Web Based Learning” at Tokyo, **Japan. Jan, 2002 – Mar, 2002.**
- Participated in Educational Fair at Colombo, **Srilanka. Aug,2004.**
- Participated in Educational Fair at **Abu Dhabi., UAE Feb, 2006.**
- Participated in School Presentations at Abu Dhabi, **Dubai and Sharjah , Dec, 2006.**
- Personal Visit to **Malaysia and Singapore** during **Dec. 2007**
- Attended EMC World Conference in **Las Vegas, USA** from **19<sup>th</sup> to 22<sup>nd</sup> May 2008**, with assistance from EMC, TEQIP and NET
- Visited **Stanford University, University of California at San Diego and California State University in San Diego** during **May 2008**
- Visited **Doha, Qatar** during **2009 April and June** on official assignment

- Presented a paper in ICCNT-2010 at **Bangkok, Thailand** on **24<sup>th</sup> April 2010**
- Presented a paper in ICCRD-2011 at **Shanghai, China** during 11-13<sup>th</sup> **March 2011**
- Presented Poster paper on “Intel Embedded Systems lab. at NMAMIT, Nitte” in **Penang, Malaysia** during Intel Academic Forum **2012**.
- Presented an invited paper on “Industry Institute Interaction in India” at **Ritsumeikan University Japan** during **March 2013**
- Attended **GEDC Conference at Chicago** on 20-22<sup>nd</sup> **October 2013**.
- Visited **Purdue University, Michigan State University and University of Wisconsin** on 23<sup>rd</sup>, 24<sup>th</sup> and 25<sup>th</sup> **October 2013**
- Attended NMAMIT Alumni Meet in **Dubai** during **March 2015**

## **15. Co-Curricular Activities**

### **15.1 Lectures Delivered**

- Talk on “Automation in IC Design” for the CSI Student branch, NMAMIT, Nitte on 21<sup>st</sup> Jan. 2016
- IUCEE Virtual academy webinar “effective use of information and communication technology in enhancing teaching learning outcomes”, Tuesday Jan. 5<sup>th</sup> 2016, 3.30PM to 4.30PM
- Lecture on “Research Issues in CSE” at Infosys campus, Mangalore during March 30<sup>th</sup> 2014
- Lecture on “Engineering Education” during the “Education & Career guidance program” arranged by “The Hindu” at Mangalore on 27<sup>th</sup> May 2012
- Lecture on “Applications of Graph theoretic algorithms in computer Science Domain - CAD FOR VLSI” at MVSIT, Moodabidri on 17<sup>th</sup> March 2012 on the occasion of 5 days VTU-VGST sponsored Faculty Development Program
- Lecture on “What an IT Engineer must be good at ? “ at Sode Madhva Vadiraja Institute of Technology and Management (SMVITM) , Bantakal on 1/3/2012

- Lecture on “Tackling barrier factors for a quality research; Application of Signal analysis and computer imaging for non-invasive diagnosis” on 9<sup>th</sup> of Feb. 2012 at ABShetty Memorial of Dental Sciences, Deralekatte, Mangalore – organized as part of workshop on “Need of Valid Research for Appropriate Clinical Care” by Dept. of Oral Biology
- Lecture on “Device Drivers” at E&C Department of NMAMIT, Nitte on 8<sup>th</sup> Feb. 2012
- Lecture on “Software issues for electrical engineering students” organized by Elites on 6<sup>th</sup> of Feb. 2012 at E&E dept. of NMAMIT, Nitte
- Lecture on “Challenges and Issues in CSE Research” at St. Joseph’s Engineering College Mangalore for the faculty members of the college on 25<sup>th</sup> June 2011.
- Keynote address during the State Level Technical Paper presentation competition “Emergia 2011” organized by IEEE students chapter, Canara engg. College on 1/4/2011
- Lecture on “VLSI design and fabrication process” at Moodlakatte Institute of Technology, Kundapura on 31/10/2010
- Keynote address on “Parallel Architecture & Programming” at Faculty development program at Sahyadri College of Engineering, Mangalore on 17/9/2010
- VTU- Edusat Lecture on “Computer concepts and C Programming” on 6<sup>th</sup> and 7<sup>th</sup> September 2010 at VTU Studio, Bangalore
- Lecture on “Recent trends in communication and Computing” at Srinivasa Institute of Computer Application and Management , Mangalore during April 2010
- Keynote address on ”Ethics in Profession” at St.Joseph’s Engg. college Mangalore in April 2010
- Lecture on “Physical and MAC Layer issues in Sensor Networks” at AICTE sponsored, IT Department organized, summer school at NITK, Surathkal on 25/7/2008

- Lecture on “Embedded and Real time Systems” at IEEE Students’ chapter of Canara College of Engg., Mangalore on 5/4/2008
- Technical Talk on “Processor IC Design from a Computer engineer’s Perspective” at VCET, Puttur on 15/3/2008
- Lectures on “Secure Data Exchange and Data Formats” and “Server Sizing and Benchmarking” to the IT officers of Corporation Bank at Bank Head Office, Mangalore , 21<sup>st</sup> and 22<sup>nd</sup> October 2007
- “E-learning”, during UGC sponsored State Level Workshop for teachers at MahaviraCollege,Moodabidri, 24/3/2007
- “ Computer Science as a Career”, at BhuvanendraCollege, Karkala, Jan 2006
- “VLSI Synthesis”, at MalnadCollege of Engineering Hassan, 16/2/2006
- “VLSI CAD and Low power VLSI issues”, at BVB college of Engineering , Hubli, 10/12/2005
- “Embedded System & RTOS”, at VivekanandaCollege of Engineering, Puttur, 24/11/2005
- “Embedded Systems and Computing”, at JawaharlalNehruCollege of Engineering, Shimoga ,8/11/2005
- “Preparing for ISO Certification and NBA Accreditation”, at KVG college of Engineering, Sullia, August 2005
- “Procurement procedure under TEQIP”, at Sir MVIT, Bangalore, June. 2005
- “IC Technologies”, at St. .Joseph’s College of Engineering, Mangalore 31/10/2004
- “Wi-Fi Technology”, during Telecommunication Day Celebrations, at DK District Telecom Office, Mangalore, 2004
- “Security issues in Data Communication” at Institute of Engineers, Local Chapter Office, Mangalore, 2004
- “Technical paper Writing”, at ISTE Chapter, NMAMIT, Nitte, 2004
- “Research Areas in Data Mining”, at MIT, Manipal, 3/8/2003
- “Compiler Design” at Dept. of MCA, NITK, Surathkal, Jan 2002

- Lecture series on “Computer Architecture “ at PDA College of Engineering, Gulbarga during 1992
- Lecture Series on “Computer Organization” at Kalpataru Institute of Technology, Tiptur during 1991.

### **15.2 Contribution to CSE Departmental Activities**

- Management responsibilities as Head of the Department of CSE
- Examiner for V.T.University Belagavi, Mangalore, Bangalore, Kuvempu, Kannur and Goa Universities

### **15.3 Contribution to College Activities**

- ISO Management Representative (MR) of NMAMIT, Nitte from 2003 to 2010
- TEQIP (Technical Education Quality Improvement Programme) Nodal officer from 2006

## **16. Publications**

### **16.1 International**

1. Luibaiba Muhammad Kunhi, Raju K, Niranjana Chiplunkar “Performance Analysis of Protein Structure Clustering Techniques and CUDA implementation of RMSD Computation” 978-1-5090-1623-5/16 2016 IEEE Conference on Distributed Computing, VLSI, Electrical Circuits and Robotics (DISCOVER), 13-14 August 2016, Published in IEEE Xplore Digital Library. Page 18-21
2. Srinivas Pai, Niranjana Chiplunkar, Srinivas Rao and Subramanya Bhat “Effective use of information and communication technology in enhancing teaching-learning outcomes: Some thoughts” presented in **International Conference** on transformation in engineering education, COEP, Pune 11<sup>th</sup> and 12<sup>th</sup> Jan, 2016 and also published in Journal of Engineering Education Transformations (JEET), 2015, ISSN 2349-2473, eISSN 2394-1707

3. Dr.Sanju, Dr.Niranjan Chiplunkar “RiCoBiT :Ring Connected Binary Tree:A structured and Scalable architecture for Network on Chip Based System”, 2015 **International Conference** on computing and network communication (CoCoNet 15), Dec. 16-19 2015, Trivandrum India, 978-1-4673-7308-1/15 IEEE 2015
4. Dr.Sanju, Dr.Niranjan Chiplunkar “Design and implementation of Mesh and torus for network on chip based systems”, Proceedings of **International Conference** on Trends in Automation , Communication and Computing Technologies (I-TACT 15), 21<sup>st</sup> and 22<sup>nd</sup> December 2015, Acharya Institute of Technology, Bangalore. 978-1-4673-6666-3/15 IEEE 2015
5. Sanju V., Niranjan Chiplunkar, Suresh Mandia, Nancy Jain, Nikita Jaiswal, M.Khalid “Design and Implementation of RiCoBiT: A structured and Scalable Architecture for Network on Chip Based Systems” **International Journal** of computing and digital Systems, ISSN 2210-142X , Vol 4 No. 1 Jan 2015, <http://dx.doi.org/10.12785/ijcnds/040107>
6. Srinivas Pai, Niranjan Chiplunkar, Srinivas Rao, Subramanya Bhat “ Undergraduate students’ research: A case study of their experience and perceptions in an autonomous Institution” eISSN: 2394-1707 , **International Conference** on Transformation in Engineering Education, 7<sup>th</sup> and 8<sup>th</sup> January 2015, BMS College of Engineering Bangalore.
7. Manjunath Kothari, Niranjan Chiplunkar and Nagesh H.R. “Implementation of Secure Customized Monitoring Tool for Adaptive Distributive Systems”, **International Conference** on Contemporary Computing and Informatics (IC3I), SJCE Mysuru 2014. 978-1-4799-6629-5/14 pp 562-567
8. Srinivas Pai and Niranjan Chiplunkar “ Success story of industry institution collaboration for enhancing teaching learning experience“ Proceedings of **International conference** on transforming engineering education ICTEE 2014, at BVB College of Engineering, Hubli. And **SPRINGER India** pp 95-102 , **DOI 10.1007/978-81-322-1931-6-14**



9. Manjunath Kothari, Sunil Manohar Dasharathi, **Dr.Niranjan Chiplunkar**, “implementation of Customized Network Monitoring Tool and Security framework Monitoring System”, **International Journal IJCET** Volume 5, Issue 9, September 2014, pp 37-49
10. Prasanna Kumar, Niranjan, Archana R.P. “ Combined operation of secret image fusion, DCT based compression and visual cryptography based encryption” **International Conference** on information and communication Technologies, PESITM, Shimoga, 2014, pp 192-197
11. Shruthi, Prasanna Kumar, Ranjan Kumar, Niranjan Chiplunkar” Image security using visual cryptography based on random grid technique and DCT” Proceedings of **International conference** on emerging trends in engineering (ICETE-2014) NMAIT, Nitte 15-17<sup>th</sup> May 2014, pp 86-91
12. Sumathi and Niranjan chiplunkar “ Dynamic search and selection of Web services” **IEEE International conference** on advanced communication, control and computing technologies (ICACCCT) 2014, ISBN : 978-1-4799-3914-5. PP 1787-1791
13. Sumathi and Niranjan “Discovery of Web services using WSDL” Proceedings of **International conference** on emerging trends in engineering ICETE 2014, NMAMIT, Nitte 15-17<sup>th</sup> May 2014
14. Sanju V. & **Niranjan Chiplunkar**, “Design & implementation of Mesh for Network on Chip Based System”, ERCICA 2014 – **International Conference** Proceedings, Elsevier Publication 2014, ISBN: 9789351072607 pp 405-409
15. Sanju V., Koushika C, Sharmili R. **Niranjan Chiplunkar**, M.Khalid, “Design and Implementation of a Network on Chip based Simulator – A performance Study”, Accepted for publication in the **Inderscience International Journal**(Indexed with impact factor)
16. Vijetha Sachin and **Niranjan Chiplunkar**, Proceedings of “SurfGaurd – Java script Instrumentation based Defense against Drive-by Downloads” ,

**International Conference** on Recent Advances in Computing and Software Systems pp 266- 271 , Chennai, India

17. **Niranjan N.Chiplunkar**, Neelima, Adarsh Konchady, Shashikiran and Brenda Olivia, “Study and Enhancement of CETUS- Source to Source compiler”. **International Conference** on Information and Communication Technology (ICICT-2011) , Dec. 24<sup>th</sup> 2011, Chennai pp 145-148
18. Venugopal, Dr.Saroja Devi, **Dr.Niranjan N.Chiplunkar**, “Survey on the issues of Digital Video Watermarking for Mobile Devices” **Internacional Journal** for Electro Computacional World Knowledge Interface Vol 1, Issue 4 Dec. 2011, ISSN No: 2249-541X (Indexing in Cabell’s Directory of Publishing, Texas USA, Ulrich’s Periodicals Directory, USA, Directory of Open Access Journals)
19. **Niranjan Chiplunkar**, Neelima, Deepak “ Multithreaded Programming Framework development for gcc infrastructure”, **3<sup>rd</sup> International conference** on Computer research and development (ICCRD 2011), Shanghai, China, 11-13<sup>th</sup> March 2011
20. B.Neelima, Dr.Prakash Raghavendra and **Dr.Niranjan N.Chiplunkar** , “ Framework for Interactive and Semi Automatic Program Optimization tool for Multi core Architecture”, **Proceedings of World Academy of Science, Engineering and Technology (WASET) Conference** , Year 7, issue 73 January 2011. Editor-in-chief: Cemal Ardil, Dubai UAE
21. Yuvaraju B.N. &**Niranjan Chiplunkar**, “Enhancing the Performance of Nodes in Ad-hoc Networks using Mobile Relay” – **International Journal of computer Science and Communications (IJCSC)**, Vol 1, No2, July-December 2010, pp 65-70

22. Yuvaraju and **Niranjan Chiplunkar** “Scenario based Performance analysis of variants of TCP using NS2 Simulator”- **International Journal of Computer Applications (IJCA)**, No. 9, august 2010 . doi: 10.5120/855-1197. pp20-24
23. Pranesh V.Kallapur and **Niranjan N.Chiplunkar**, “Topology aware Mobile agent for efficient data collection in wireless sensor networks with dynamic deadlines”, **International Conference** on Advances in Computer Engineering, 20-21<sup>st</sup> June 2010, Bangalore, DOI: <http://doi.ieeecomputersociety.org/10.1109/ACE.2010.26>
24. Yuvaraju and **Niranjan Chiplunkar**, “PICCO: Protocol Independent Congestion control Method using Mobilerelays for Adhoc Networks” , Proceedings of **ICCNT**, Bangkok **Thailand** 24<sup>th</sup> April 2010, pp 93-97
25. Ashok Kumar, **Niranjan Chiplunkar** and Goverdhan Hegde , “Sequential and Simultaneous Router for 3D FPGA”, **IJCSNS International Journal** of Computer science and network security (**IJCSNS**), Vol9, nO.2, Feb 2009 pp258-263
26. Ashok Kumar, **Niranjan Chiplunkar** and Goverdhan Hegde “Detailed Router for #D FPGA using Prioritized Auction Based Algorithm” **International Journal** of computer Science and Knowledge engineering (**IJCSKE**) , vol 3 No.1, Jan-June 2009, pp97-100
27. Ashok Kumar, **Niranjan chiplunkar** and Vinay “Detailed router for 3D FPGA using Sequential and simultaneous approach” Proceedings of **International Multiconference** of Engineers and Computer Scientists 2009, vol-II, IMECS, March18-20, 2009, **HongKong**, ISBN :978-988-17012-7-5

28. Sanju, **Niranjan N. Chiplunkar**, Routing Through Network On Chip, **International Conference** on “Recent Trends in Computational Science”, Toc-H Institute of Science & Technology, Kochi , Kerala, on June 11-13, 2008
29. Sanju & **Niranjan N. Chiplunkar** “Network On Chip: A Brief Review” At **International Conference** On ‘Emerging Techniques in Computing, Electronics, Embedded System & VLSI Design’ ICEVD 2008 Conducted By Padmashri Dr. Vithalrao Vikhe Patil College Of Engineering, Ahmednagar, From 20<sup>th</sup> Mar 2008 To 21<sup>th</sup> Mar 2008
- 30. Niranjan N. Chiplunkar**, Ramakrishna, 2006. The role of Information Technology in exchange and management of knowledge by the Corporate and Institutions. Accepted for presentation at IEEE International Engineering Management Conference 2006 at Salvador, **Brazil**, 17<sup>th</sup> - 20<sup>th</sup> Sep, 2006.
31. Ashok Kumar & **Niranjan Chiplunkar** “ABR: Auction Based Router for 3D FPGA”, **First International conference** on “Frontier Technologies- Need for Business, Industry and Education”, Adhiyamaan College of Engineering, Hosur, TN, September 2006
31. Yuvaraju, Manjunath Kotari, **Niranjan N. Chiplunkar**., 2006. Security issues in wireless network, Proceedings of **3<sup>rd</sup> International Conference** ObCom-2006 on Mobile, Ubiquitous & Pervasive Computing, Dec. 16<sup>th</sup> - 19<sup>th</sup> , 2006, VIT, Vellore, India. Volume-II, 210-212pp.
32. H.R. Nagesh, K. Chandrashekhara and **Niranjan N. Chiplunkar**. SURAKSHA: An implementation of Secure Group Communications. Proc. Of **12<sup>th</sup> International conference** on “Advanced Computing and Communications, Nirma Inst. Of Technology, Ahmedabad. 286-294pp.

33. **Niranjan N. Chiplunkar** & Chitrasena Bhat, Heuristic technology mapper for LUT based FPGAs. Proc. Of IEEE's **12<sup>th</sup> International Conf.** On VLSI Design, Goa, India Jan. 1999. 390-393pp. Published in ACM portal by IEEE Computer Society, Washington, DC, USA. ISBN:0-7695-0013-7. DOI Bookmark: <http://doi.ieeecomputersociety.org/10.1109/ICVD.1999.745187>
34. Sumathi, **Niranjan N Chiplunkar** "Necessity of Dynamic Integration for Web Services", Paper presented at **International Conference** on Applied and Theoretical Computing and Communication Technology IEEE-2015.
35. Sumathi, Dr. **Niranjan N Chiplunkar** "Dynamic search and selection of Web Services", **International Conference** on Applied and Theoretical Computing and Communication Technology, TamilNadu, India IEEE-2015.
36. Sumathi, **Dr. Niranjan N Chiplunkar** "Discovery of Web Services using WSDL", **International conference** on Emerging Trends in Engineering (ICETE-2014) in NMAMIT, Nitte , 15-17 May, 2014.
37. Anisha P Rodrigues, **Niranjan N. Chiplunkar**, Anujna Rao "Sentiment Analysis of Social Media Data using Hadoop Framework: A Survey" published in IJCA(International Journal of Computer Applications) ", Volume 151 – No.6, October 2016
38. Bhavitha BK, Anisha P Rodrigues, **Dr. Niranjan N Chiplunkar** "Comparative study of machine learning techniques in Sentimental analysis", International Conference on Inventive Communication and Comoutational Technologies(ICICCT-2017), held during 10-11, March 2017, at the Ranganathan Engineering College, Coimatore, India, to be published in IEEE Explore.
39. Anisha P Rodrigues, **Dr. Niranjan N Chiplunkar** "Big data Mining :A survey on techniques involved in sentimental analysis", 13<sup>th</sup> International IEEE India conference INDIACOM-2017 organized by BVICAM, New Delhi during 01-03 March 2017, To be published in IEEE Explorer.

40. Anisha P Rodrigues, **Dr. Niranjana N Chiplunkar** “Mining Online Product Reviews and Extracting Product features using Unsupervised method”, 13<sup>th</sup> International IEEE India conference INDICON-2016 organized by IEEE india chapter at IISc Bangalore during 16-18 December 2016, DOI:10.1109/INDICON.2016.7839054, ISBN Information: Electronic ISBN: 978-1-5090-3646-2 ,Print on Demand(PoD) ISBN: 978-1-5090-3647-9 Electronic ISSN: 2325-9418
41. Sumathi, **Dr. Niranjana N Chiplunkar**, “Populating Parameters of Web Services by Automatic Composition Using Search Precision and WSDL Weight Matrix”, IJCSE, 1742-7193, InderScience Publishers – Scopus indexed, (in press:Accepted on July 2016).
42. Sumathi, **Dr. Niranjana N Chiplunkar**, “Dynamic Composition of Web Services by Service Invocation Dynamically” , IJEME ISSN: 2305-3623(Print), ISSN: 2305-8463, July 2017, DOI: 10.5815/ijeme.2017.04.05, pp 41-50 (open access).
43. Sumathi, **Dr. Niranjana N Chiplunkar** “Open Source APIs for Processing the XML Result of Web Services”, International Conference on Advances in Computing, Communications and Informatics (ICACCI) Paper-ID—2017 Paper ID : 1570363306 (Scopus indexed & Thomson Reuters indexed conference). Paper Category: Regular Paper (Oral Presentation), MIT – Manipal, IEEE-2017.
44. Durga Prasad, **Niranjana N.Chiplunkar**, K.Prabhakar Nayak, “*Channel modeling and performance analysis of shift keying modulation techniques in various fading channels of wireless body area networks*”, International Conference of Recent Trends in Engineering & Technology (ICRTET-2013) held on 2 -3 January 2013 at Park View Inn, Bangalore, pp 55-60.
45. Durga Prasad, **Niranjana N.Chiplunkar**, K.Prabhakar Nayak, “*Bit error rate analysis for various modulation techniques in different channels in WBAN*”, International Conference on Research Trends in Computer Technologies (ICRTCT 2013) held on 30 -31 January 2013 at Dr.N.G.P Arts and Science College, Coimbatore(T.N), pp 325-330.
46. Durga Prasad, **Niranjana N.Chiplunkar**, K.Prabhakar Nayak, “*Performance Analysis of the Physical and Medium Access Control Layer Parameters with Effect of Varying Transmission Power using IEEE 802.15.4 Standard for Wireless Body Sensor Network*”, 6th International Conference On Advances In

Computing & Communications, ICACC 2016, held on 6-8 September 2016, at Cochin, India, available at Elsevier's Science Direct Procedia of Computer Science, pp. 639 – 646.

47. Durga Prasad, **Niranjan N. Chiplunkar**, K.Prabhakar Nayak, “A *Trusted Ubiquitous Healthcare Monitoring System for Hospital Environment.*”, International Journal of Mobile Computing and Multimedia Communication, (IJMCMC), IGI Global Publications. Volume 8, Issue 2, Article 2.
48. Prashant G Desai, Sarojadevi, , **Niranjan N Chiplunkar** “A Study of Natural Language Processing Based Algorithms for Text Summarization”, Proceedings of International Journal of Innovative Research in Science, Engineering and Technology, ISSN : 2347-6710, Vol.5, Issue-11, 2016, pp 304-308
49. Prashant G Desai, Sarojadevi, , **Niranjan N Chiplunkar** “A Template Based Algorithm For Automatic Summarization And Dialogue Management For Text Documents”, Proceedings of International Journal of Research in Engineering and Technology, ISSN : 2321-7308, Vol.4, Issue-11, 2015, pp 334-340
50. Mahesh Kini M, Prashant G Desai, Sarojadevi, , **Niranjan N Chiplunkar** “Text Mining Approach to Classify Technical Research Documents using Naïve Bayes”, Proceedings of International Journal of Advanced Research in Computer and Communication Engineering, ISSN : 2278-1021, Vol.4, Issue-7, 2015, pp 386-391
51. Prashant G Desai, Sarojadevi, , **Niranjan N Chiplunkar** “Rule-Knowledge Based Algorithm for Event Extraction”, Proceedings of International Journal of Advanced Research in Computer and Communication Engineering, ISSN : 2319-5940, Vol.4, Issue-1, 2015, pp 79-85
52. Prashant G. Desai, Sarojadevi H., and **Niranjan N. Chiplunkar** , “An NLP Based Approach for Extracting Intelligence from Event Documents”, Current Trends in Technology and Sciences, ISSN:2279-0535, Vol.III(1) , January 2014, pp49-53

53. Prashant G. Desai, **Niranjan N. Chiplunkar** and Sarojadevi, “An Approach for POS Tagging for a Conversational Software System”, International conference on Research Trends in Computer Technologies, January 2013, pp 423-425
54. Prashant G Desai, Sarojadevi, **Niranjan Chiplunkar**, “Survey of Concepts used in Natural Language Processing”, Proceedings of the ICETE, May 2012, pp 85-87
55. Sunitha N V, Raju K, and **Niranjan N Chiplunkar**, “Performance Improvement of CUDA Applications by Reducing CPU-GPU Data Transfer Overhead”, ICICCT-2017, REC, Coimbatore., IEEE/ ISBN: 978-1-5090-5297-4/17 ©2017 IEEE.
56. Sahana Karanth, Raju K., **Niranjan N Chiplunkar**, “Experimental Evaluation of Effects of Thread Distribution on GPU”, ICETE-15, NMAMIT Nitte, 8-9 May 2015.

## 16.2 National

57. **Dr. Niranjan Chiplunkar** and Srinivas Pai “Use of Videos to facilitate effective learning in technical Institute : An Institutional Perspective”, Page No. 13 , Abstracts of Technical Papers, Indian Technology Congress 2016, 1-2<sup>nd</sup> December 2016, Jnanjyothi Auditorium, Central College Campus, Bangalore
58. Raju K., **Niranjan Chiplunkar**, Sahana Karanth “Multithreaded implementation of Data Intensive Applications with Overlapped I/O and Computation”, National Conference on Parallel Computing Technologies “PARCOMPTECH India 2015”, CDACBangalore , Feb20th 2015, pp 177-190
59. Srinivas Pai and **Niranjan Chiplunkar** “ Understanding the influence of faculty research on student’s learning and performance” ISTE National Convention 2014, College of Engineering Trivandrum, December 2014.



- 60.** Manjunath Kothari, Dr.HRNagesh and **Dr.Niranjan** “Security aspects of adaptive distributed systems and proposed solutions”, in ANUSANDHANA , Journal of Science,Engineering and Management, Vol 1, issue 1, March 2012 ISSN: 2231-4032, NMIT, Bangalore pp 45-49
- 61.** Yuvaraju and **Niranjan Chiplunkar**, “Security issues in wireless Data Connectivity for the Library Information system, IT Poilcy and enforcement by Cyber Law”, Proceedings of National Seminar on “In search of information Power – future Challenges for Techno Libraries” held at NMAMIT, Nitte September 11<sup>th</sup> and 12<sup>th</sup> 2008
- 62.** Ashok Kumar, **Niranjan Chiplunkar** and Prasanna Kumar . “Techno Libraries: A User Perspective”, Proceedings of National Seminar on “In search of information Power – future Challenges for Techno Libraries” held at NMAMIT, Nitte September 11<sup>th</sup> and 12<sup>th</sup> 2008
- 63.** Ashok Kumar &**Niranjan Chiplunkar** “ Auction based Detailed router for 3D FPGA” , NC-Vcom SAINT, GITS college of Engineering, Kerala, March 2008
- 64.** Ashok Kumar &**Niranjan N.Chiplunkar** “Auction Based Detailed Router Implementation for 3D FPGA”, NCCT’08, Mepco Schlenk Engg. College, Sivakash, March 2008
- 65.** Sanju & **Niranjan Chiplunkar** “Network On Chip: The Gennext Technology For Billion Transistor Fabrication - A Review” At National Conference On Ubiquitous & Pervasive Computing Conducted By NMAMIT, Nitte From 24<sup>th</sup> Jan 2008 To 25<sup>th</sup> Jan 2008
- 66.** **Niranjan N. Chiplunkar** & B Neelima on “ Analysis of Threaded Applications on Multi-core Platform”, 2008.At the TEQIP sponsored National Level

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69. Ashok Kumar, Ahmed Shafiq, **Niranjan N. Chiplunkar**, “Auction based detailed router for 3D FPGAs”, National Conference for PG & PhD Scholars – PGFEST 2006, NMAMIT, Nitte 28-29<sup>th</sup> April 2006. 22pp.
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71. Yuvaraju B.N & **Niranjan N. Chiplunkar**, “Security in Manets using Bioinformatics” State Level Symposium on ”Recent Trends in Biotechnology”, NMAMIT, Nitte 15,16<sup>th</sup> Sept. 2005 “Parichay”.9pp.
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- 81.** Ganapathi Hegde & **Niranjan N. Chiplunkar**, ”Multi FPGA system and its applications”, Proc. Of PGFEST 2003, 25-26th July 2003, NMAMIT, Nitte. 35pp.
- 82.** **Niranjan N. Chiplunkar**, “E-Learning:Use of IT in Teaching Learning Process”, Proceedings of 32<sup>nd</sup> National Convention of ISTE(Indian Society for Technical Education), 27-29, Dec.2002. 62-63pp.
- 83.** **Niranjan N. Chiplunkar**, “Review of Spatial DataBases”, Proc. Of National Conference on Recent Trends in DBMS, concepts & Practice, Nitte,India, March 2002. 209-216pp.
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- 86.** **Niranjan N. Chiplunkar**& Manoj V.N, ”Computational Aspects of certain Placement and Routing algorithms of VLSI system”, Proc. Of 2<sup>nd</sup> National Conference on Advanced Computing, PSG Tech, Coimbatore, India, Feb. 2002. 51-55pp.

- 87. Niranjan N. Chiplunkar** & Chitrasena Bhat, "Algorithm for Area Optimization in LookupTable based FPGA Technology mapping", Proc. Of annual CSI convention, NewDelhi, Sept. 1998. 149-155pp.
- 88. Niranjan N. Chiplunkar**, "Technology mapping in FPGAs", Proc. Of National Conference on IT for 21<sup>st</sup> Century, Surathkal, India, 19-21st Dec.1996. 32-38pp.
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Online ISSN : 0974-5645, Indian Journal of Science & Technology, Vol 10(17), DOI: 10.17485/ijst/2017/v10i17/108890, May 2017 pp 1-11 (indexed by Thomson Reuters "Web of Science")
- 94. Durga Prasad, Niranjan N.Chiplunkar**, K.Prabhakar Nayak, "*Energy Efficient Routing Solution (EERS) for Wireless Body Sensor Network Considering Critical Situations*", Asian Journal of Information Technology, Medwells Publications, Year: 2017, Volume: 16, Issue: 1 , Page No.: 82-87, DOI: [10.3923/ajit.2017.82.87](https://doi.org/10.3923/ajit.2017.82.87)

## 17. Books& Proceedings

- “VLSI CAD” Text book – Niranjan N.Chiplunkar & Manjunath Kothari, PHI Learning New Delhi, **May 2011** (ISBN: 978-81-203-4286-6)
- “CAD for VLSI” Text Book- **Niranjan N.Chiplunkar&Manjunath Kothari, NMAMIT Publication, February, 2007** (ISBN : 978-81-7525-820-4)
- Member of Editorial Board of NMAMIT Annual Research Journal (ISSN: 2249-0426)
- Edited “*Proceedings of 32<sup>nd</sup> National Convention of Indian Society for Technical Education*” , B.S.Samaga, **Niranjan N.Chiplunkar**, Dec. 2002
- Edited “Proceedings of International conference & Symposium on Storage Technologies (ICSST)” **Niranjan Chiplunkar**, Mr.Manjunath Kothari, 2008 (ISBN: 978-81-907846-0-3)
- Edited “Proceedings of International conference on Computer Architecture, Networking and Applications (ICCANA)” **Niranjan Chiplunkar**, Dr.Uday Kumar Shenoy , 2011(ISBN: 93-80697-42-2), published by Excel India Publishers, New Delhi

#### **18. Research Grants Obtained:**

- Intel-FICE academic fund of **Rs.2.00lakh** for setting up “Embedded Application lab. Using Intel ATOM processor” November 2011 . Also 10 Intel ATOM kits have been received costing about Rs. 2 lakhs.
- DST Project titled “Software architecture and programming model to enhance the performance of application on multi-core platform” (DST/TDD/03) for 2 years from Jan 1st 2010 to Dec. 31<sup>st</sup> 2011. Amount sanctioned :**Rs.16.00 lakhs**(As Principal Investigator)
- AICTE-RPS(B) project titled “Study and design of algorithms for VLSI CAD tool development towards applications involving mutually exclusively modules” for 2 years from Dec. 1<sup>st</sup> 2008 to 30<sup>th</sup> November 2010, Amount sanctioned: **Rs.4.6lakhs** (As Co-Principal Investigator)

#### **19. Research Interests**

Algorithm design for VLSI CAD, Multi core architecture and Program Optimization, Ad-hoc Networks, E-learning, Advanced Microprocessors

## 20. Subjects Taught

Logic Design, Design and Analysis of Algorithms, Finite Automata & formal Languages, Computer Organization & Architecture, Systems Programming, RDBMS, Microprocessors & Peripherals, Principles of Programming Languages, compiler Design, Computer Graphics, VLSI Design, C# Programming & .NET Concepts, Embedded and Real Time Systems, Cryptography and Network Security, MIS, CAD for VLSI, System simulation and Modelling

## 21. Ph.Ds guided (3) /guiding (10)

- Dr.Ashok Kumar - Ph.D.completed- VTU, Belagavi – 2010
- Dr.Yuvaraju – Ph.D. completed – VTU, Belagavi-2012
- Dr.Sanju, -Ph.D. completed , VIT, Vellore- 2015
- Mr.Prasanna Kumar – Ph.D. ongoing- VTU - Guide
- Mr.Manjunath Kothari – Ph.D. ongoing – VTU -Guide
- Mr. Durga Prasad – Ph.D. ongoing – Manipal Univerversity – Guide
- Ms. Sumathi Pawar – Ph.D. ongoing –VTU -Guide
- Mr.Raghavendra Rao – Ph.D. ongoing –VTU -Guide
- Ms. Anisha Rodrigues – Ph.D. ongoing –VTU –Guide
- Mr.Raju-Ph.D. ongoing – VTU -Guide
- Mr.Karthik Pai – Ph.D. ongoing – Co-guide
- Mr.Venugopal –Ph.D. ongoing –Co-guide
- Mr.Prashanth Desai –Ph.D. ongoing –Co-guide

## 22. Miscellaneous

- Developed “Computer Graphics Laboratory” at Manipal Institute Technology, Manipal, India in March 1988
- Reviewer for the Proceedings of IEEE Conference on “VLSI Design” since 2002.
- Chair of Programme Committee, National Conference on “Distributed Computing” , NMAMIT, Nitte India, 2004
- Coordinator for *FIVE* ISTE sponsored Short Term Training Programs

- Set up “Data Mining Laboratory” at NMAM Institute of Technology, Nitte during Jan. 2006
- Chair of Organizing Committee , National Conference on “Ubiquitous & Pervasive Computing” , NMAMIT, Nitte, India, 24-25, Jan, 2007
- So far, guided 42 BE level projects and 17 M.Tech. level projects
- President of Rotary Club of Nitte during 2006-07
- Consultant for a Patent Search organization :EntellectLLP.com
- **Applied for an Indian Patent**, based on research work (application no.: 428/CHE/2008)
- **Applied for US Provisional Patent** in April 2010 for the research work on “New Architecture for Network on chip (NoC) systems” (Application Number:61/395,480 dated 14/5/2010)
- Received AICTE travel grant for attending the conference at Shanghai, China
- Was Chairman of BOE (Board of Examination)–Computer Science at Mangalore University during 1997-98. Was member of BOE-Computer Science of Visvesvaraya Technological University ,Belgaum during 2004-05.
- BOS and BOE Chairman of NMAMIT Nitte for Computer Science UG and PG programs under Autonomous Scheme.
- Board of Studies member of Computer Science at MIT, ManipalUniversity, NMIT Bangalore, SDMCE Dharwad, BVBCE Hubli, Bangalore, Dr.AIT Bangalore and St.Aloysius College Mangalore during 2007-08
- Governing Council Member of St.John Engg. College Bangalore, Academic Council Member of Dr.AIT Bangalore from 2013.
- President of Hockey India Udupi from 2014 July
- Program Chair of 2016 IEEE Distributed Computing, VLSI, Electrical Circuits and Robotics (DISCOVER) Conference, 13-14 August 2016, NITK, Surathkal



### **23. Seminars/conferences /Continuing education programs Attended:**

#### International:

- EMC World conference at Las Vegas USA 19-22 May 2008
- Conference on Computer Networking, Bangkok, Thailand, 24<sup>th</sup> April 2010
- Conference on Computer Research and Development , Shanghai, China , 10-12March 2011.

#### **National**

- Ten days Faculty development program at IISc on “Computer Graphics and CAD” during April 1988
- Three days training program on “Software Engineering” at Infosys Bangalore during August 1992. Resource Person :Prof. H.N.Mahabala
- Five days STTP on “Advanced JAVA at PSG, coimbatore during 2001
- Five days STTP on “COM & DCOM” at NITK, Surathkal during November 2005
- Three days TEQIP Workshop on Financial Management at Indian Institute of Financial Management at Faridabad during November 2006
- Five days IUCEE workshop on “Advanced compilation Technicque” at Infosyscampus Mysore during June 2008. Resource person: Prof. Jack Davidson, from Virginia Tech., USA
- Five days IUCEE workshop on “Advanced Microprocessors” at Infosys campus, Mysore during June 2009. Resource person : Prof. Rafiqzmann, California Polytechnic at Pamola

- Many more one day/two day programs at NMAMIT Nitte, MIT Manipal , NITK surathkal, MSRIT Bangalore and other colleges
- One day Faculty development program on “Predictive Analytics” at Accenture Services Bangalore on 16<sup>th</sup> December 2015

**24. Seminars/Conferences/continuing education programs Organized:**

- AICTE-STTP on “Theory & Practice of CAD for VLSI” – 2-14<sup>th</sup> Sept. 2002
- AICTE-ISTE Winter school on “Object Oriented and visual programming” 17-29<sup>th</sup> Jan 2000
- AICTE-ISTE STTP on “E-learning, XML and Multimedia”- 9-21<sup>st</sup> Feb 2004
- National Conference on “Mobile and Pervasive computing”, 16-17 Sept. 2009
- 22<sup>nd</sup> CSI Karnataka Student Convention- 7<sup>th</sup> Nov. 2008
- National Conference on Ubiquitous computing, in 2006
- National level Workshop on “Device Drivers on Multiplatforms” in 2007
- International Conference on “Storage Technologies” 3-4 Dec. 2008
- International conference on “Computer Architecture, Networking and Applications”, Jan 7-8, 2011
- International Conference on “Emerging Trends in Engineering”, May 2012
- Many more one day/two day programs at NMAMIT, Nitte for the teaching/non teaching staff of NMAMIT and other colleges.

