

## Bio-Data

1. **Name** : NIRANJAN N. CHIPLUNKAR  
2. **Qualification** : B.E, M.Tech, Ph.D  
3. **Sex** : Male  
4. **Marital Status** : Married  
5. **Date of Birth** : 27<sup>th</sup> Dec, 1964  
6. **Category** : General  
7. **Nationality** : Indian  
8. **Present Position** : Principal,  
NMAMIT, Nitte

9. **Address for communication** :

Principal

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**10. Academic Record:**

- Graduation** : B.E in E&C from NIE, Mysore, India, 1986 -(FCD)  
**Post-Graduation** : M.Tech in CSE from MIT, Manipal, India, 1990-(FCD)  
**Doctorate** : Ph.D in CSE from University of Mysore, India, 2002  
**Area of Research** : CAD for VLSI  
**Topic** : Design of Algorithms for optimum area  
utilization in VLSI ASICS

## 11. Experience:

<b>From</b>	<b>To</b>	<b>Organization</b>	<b>Designation</b>	<b>Total Period</b>
1987	1993	Manipal Institute of Technology (MIT), Madhav Nagar Manipal, Karnataka, 576104	Lecturer	6Years
1993	1996	Manipal Institute of Technology (MIT), Madhav Nagar Manipal, Karnataka, 576104	Reader	3 Years
1997	2003	NMAMIT, P.O: Nitte Karkala Taluk, Udupi District, Karnataka, India-574 110	Professor	7 years
2003	2012	NMAMIT, P.O: Nitte Karkala Taluk, Udupi District, Karnataka, India-574 110	Professor & Head of the Department	9years
2010	2012		Dean(Acade mics)	

13 <sup>th</sup> June 2012	Till date	Principal, NMAIT, Nitte		
Total				27 years

## 12. Membership in Professional Bodies

- Life Member of ISTE
- Senior Member of IEEE
- Life Member of CSI
- Fellow of IE(India)

## 13. Awards and Recognitions

- “Bharatiya Vidya Bhavan National award for Best Engineering College Principal 2014” given by ISTE
- “Excellent Achievement Award” by Centre for International Cooperation on Computerization (CICC), Govt. of Japan in 2002
- “Outstanding Alumnus of MIT, Manipal”, by Manipal University, Manipal, India in 2008
- Resume is published in “Marquis Who’s Who in World” in January 2009

## 14. Overseas Experience

- Attended a training on “Web Based Learning” at Tokyo, **Japan**. Jan, 2002 – Mar, 2002.
- Participated in Educational Fair at Colombo, **Srilanka**. Aug, 2004.
- Participated in Educational Fair at **Abu Dhabi**., UAE Feb, 2006.

- Participated in School Presentations at Abu Dhabi, **Dubai and Sharjah** , Dec, 2006.
- Personal Visit to **Malaysia and Singapore** during Dec. 2007
- Attended EMC World Conference in **Las Vegas, USA** from 19<sup>th</sup> to 22<sup>nd</sup> May 2008, with assistance from EMC, TEQIP and NET
- Visited **Stanford University, University of California at San Diego and California State University in San Diego** during May 2008
- Visited **Doha, Qatar** during 2009 April and June on official assignment
- Presented a paper in ICCNT-2010 at **Bangkok, Thailand** on 24<sup>th</sup> April 2010
- Presented a paper in ICCRD-2011 at **Shanghai, China** during 11-13<sup>th</sup> March 2011
- Presented Poster paper on “Intel Embedded Systems lab. at NMAMIT, Nitte” in **Penang, Malaysia** during Intel Academic Forum 2012.
- Presented an invited paper on “Industry Institute Interaction in India” at **Ritsumeikan University Japan** during March 2013
- Attended **GEDC Conference at Chicago** on 20-22<sup>nd</sup> October 2013.
- Visited **Purdue University, Michigan State University and University of Wisconsin** on 23<sup>rd</sup>, 24<sup>th</sup> and 25<sup>th</sup> **October 2013**

## 15. Co-Curricular Activities

### 15.1 Lectures Delivered

- Lecture on “Research Issues in CSE” at Infosys campus, Mangalore during March 30<sup>th</sup> 2014
- Lecture on “Engineering Education” during the “Education & Career guidance program” arranged by “The Hindu” at Mangalore on 27<sup>th</sup> May 2012
- Lecture on “Applications of Graph theoretic algorithms in computer Science Domain - CAD FOR VLSI” at MVSIT, Moodabidri on 17<sup>th</sup> March 2012 on the occasion of 5 days VTU-VGST sponsored Faculty Development Program

- Lecture on “What an IT Engineer must be good at ? “ at Sode Madhva Vadiraja Institute of Technology and Management (SMVITM) , Bantakal on 1/3/2012
- Lecture on “Tackling barrier factors for a quality research; Application of Signal analysis and computer imaging for non-invasive diagnosis” on 9<sup>th</sup> of Feb. 2012 at ABShetty Memorial of Dental Sciences, Deralekatte, Mangalore – organized as part of workshop on “Need of Valid Research for Appropriate Clinical Care” by Dept. of Oral Biology
- Lecture on “Device Drivers” at E&C Department of NMAMIT, Nitte on 8<sup>th</sup> Feb. 2012
- Lecture on “Software issues for electrical engineering students” organized by Elites on 6<sup>th</sup> of Feb. 2012 at E&E dept. of NMAMIT, Nitte
- Lecture on “Challenges and Issues in CSE Research” at St. Joseph’s Engineering College Mangalore for the faculty members of the college on 25<sup>th</sup> June 2011.
- Keynote address during the State Level Technical Paper presentation competition “Emergia 2011” organized by IEEE students chapter, Canara engg. College on 1/4/2011
- Lecture on “VLSI design and fabrication process” at Moodlakatte Institute of Technology, Kundapura on 31/10/2010
- Keynote address on “Parallel Architecture & Programming” at Faculty development program at Sahyadri College of Engineering, Mangalore on 17/9/2010
- VTU- Edusat Lecture on “Computer concepts and C Programming” on 6<sup>th</sup> and 7<sup>th</sup> September 2010 at VTU Studio, Bangalore
- Lecture on “Recent trends in communication and Computing” at Srinivasa Institute of Computer Application and Management , Mangalore during April 2010
- Keynote address on ”Ethics in Profession” at St.Joseph’s Engg. college Mangalore in April 2010

- Lecture on “Physical and MAC Layer issues in Sensor Networks” at AICTE sponsored, IT Department organized, summer school at NITK, Surathkal on 25/7/2008
- Lecture on “Embedded and Real time Systems” at IEEE Students’ chapter of Canara College of Engg., Mangalore on 5/4/2008
- Technical Talk on “Processor IC Design from a Computer engineer’s Perspective” at VCET, Puttur on 15/3/2008
- Lectures on “Secure Data Exchange and Data Formats” and “Server Sizing and Benchmarking” to the IT officers of Corporation Bank at Bank Head Office, Mangalore , 21<sup>st</sup> and 22<sup>nd</sup> October 2007
- “E-learning”, during UGC sponsored State Level Workshop for teachers at Mahavira College, Moodabidri, 24/3/2007
- “Computer Science as a Career”, at Bhuvanendra College, Karkala, Jan 2006
- “VLSI Synthesis”, at Malnad College of Engineering Hassan, 16/2/2006
- “VLSI CAD and Low power VLSI issues”, at BVB college of Engineering , Hubli, 10/12/2005
- “Embedded System & RTOS”, at Vivekananda College of Engineering, Puttur, 24/11/2005
- “Embedded Systems and Computing”, at Jawaharlal Nehru College of Engineering, Shimoga ,8/11/2005
- “Preparing for ISO Certification and NBA Accreditation”, at KVG college of Engineering, Sullia, August 2005
- “Procurement procedure under TEQIP”, at Sir MVIT, Bangalore, June. 2005
- “IC Technologies”, at St. Joseph’s College of Engineering, Mangalore 31/10/2004
- “Wi-Fi Technology”, during Telecommunication Day Celebrations, at DK District Telecom Office, Mangalore, 2004
- “Security issues in Data Communication” at Institute of Engineers, Local Chapter Office, Mangalore, 2004
- “Technical paper Writing”, at ISTE Chapter, NMAMIT, Nitte, 2004

- “Research Areas in Data Mining”, at MIT, Manipal, 3/8/2003
- “Compiler Design” at Dept. of MCA, NITK, Surathkal, Jan 2002
- Lecture series on “Computer Architecture “ at PDA College of Engineering, Gulbarga during 1992
- Lecture Series on “Computer Organization” at Kalpataru Institute of Technology, Tiptur during 1991.

### **15.2 Contribution to CSE Departmental Activities**

- Management responsibilities as Head of the Department of CSE
- Examiner for V.T.University, Mangalore, Bangalore, Kuvempu, Kannur and Goa Universities

### **15.3 Contribution to College Activities**

- ISO Management Representative (MR) of NMAMIT, Nitte from 2003 to 2010
- TEQIP (Technical Education Quality Improvement Programme) Nodal officer from 2006

## **16. Publications**

### **16.1 International**

1. Srinivas Pai, Niranjana Chiplunkar, Srinivas Rao, Subramanya Bhat “ Undergraduate students’ research: A case study of their experience and perceptions in an autonomous Institution” eISSN: 2394-1707 , International Conference on Transformation in Engineering Education, 7<sup>th</sup> and 8<sup>th</sup> January 2015, BMS College of Engineering Bangalore.
2. Srinivas Pai and Niranjana Chiplunkar “ Success story of industry institution collaboration for enhancing teaching learning experience“ ICTEE 2014, at BVB College of Engineering, Hubli.

3. Manjunath Kothari, Sunil Manohar Dasharathi, **Dr.Niranjan Chiplunkar**, “implementation of Customized Network Monitoring Tool and Security framework Monitoring System”, **IJCET** Volume 5, Issue 9, September 2014, pp 37-49
4. Prasanna Kumar, Niranjan, Archana R.P. “ Combined operation of secret image fusion, DCT based compression and visual cryptography based encryption” International Conference on information and communication Technologies, PESITM, Shimoga, 2014, pp 192-197
5. Shruthi, Prasanna Kumar, Ranjan Kumar, Niranjan Chiplunkar” Image security using visual cryptography based on random grid technique and DCT” Proceedings of International conference on emerging trends in engineering (ICETE-2014) NMAIT, Nitte 15-17<sup>th</sup> May 2014, pp 86-91
6. Sumathi and Niranjan chiplunkar “ Dynamic search and selection of Web services” IEEE International conference on advanced communication, control and computing technologies (ICACCCT) 2014, ISBN : 978-1-4799-3914-5. PP 1787-1791
7. Sumathi and Niranjan “Discovery of Web services using WSDL” Proceedings of ICETE 2014, NMAMIT, Nitte 15-17<sup>th</sup> May 2014
8. Sanu V. & **Niranjan Chiplunkar**, “Design & implementation of Mesh for Network on Chip Based System”, ERCICA 2014 – International Conference Proceedings, Elsevier Publication 2014, ISBN: 9789351072607 pp 405-409
9. Sanju V., Koushika C, Sharmili R.**Niranjan Chiplunkar**, M.Khalid, “Design and Implementation of a Network on Chip based Simulator – A performance Study”, Accepted for publication in the **Inderscience International Journal** (Indexed with impact factor)
10. Vijetha Sachin and **Niranjan Chiplunkar**, Proceedings of “SurfGaurd – Java script Instrumentation based Defense against Drive-by Downloads” , International Conference on Recent Advances in Computing and Software Systems pp 266- 271 , Chennai, India



11. **Niranjan N.Chiplunkar**, Neelima, Adarsh Konchady, Shashikiran and Brenda Olivia, “Study and Enhancement of CETUS- Source to Source compiler”. International Conference on Information and Communication Technology (ICICT-2011) , Dec. 24<sup>th</sup> 2011, Chennai pp 145-148
12. Venugopal, Dr.Saroja Devi, **Dr.Niranjan N.Chiplunkar**, “Survey on the issues of Digital Video Watermarking for Mobile Devices” **Internacional Journal** for Electro Computacional World Knowledge Interface Vol 1, Issue 4 Dec. 2011, ISSN No: 2249-541X (Indexing in Cabell’s Directory of Publishing, Texas USA, Ulrich’s Periodicals Directory, USA, Directory of Open Access Journals)
13. **Niranjan Chiplunkar**, Neelima, Deepak “ Multithreaded Programming Framework development for gcc infrastructure”, 3<sup>rd</sup> International conference on Computer research and development (ICCRD 2011), Shanghai, China, 11-13<sup>th</sup> March 2011
14. B.Neelima, Dr.Prakash Raghavendra and **Dr.Niranjan N.Chiplunkar** , “ Framework for Interactive and Semi Automatic Program Optimization tool for Multi core Architecture”, Proceedings of World Academy of Science, Engineering and Technology (WASET) Conference , Year 7, issue 73 January 2011. Editor-in-chief: Cemal Ardil, Dubai UAE
15. Yuvaraju B.N. & **Niranjan Chiplunkar**, “Enhancing the Performance of Nodes in Ad-hoc Networks using Mobile Relay” – **International Journal of computer Science and Communications (IJCSC)**, Vol 1, No2, July-December 2010, pp 65-70
16. Yuvaraju and **Niranjan Chiplunkar** “Scenario based Performance analysis of variants of TCP using NS2 Simulator”- **International Journal of Computer Applications (IJCA)**, No. 9, august 2010 . doi: 10.5120/855-1197. pp20-24
17. Pranesh V.Kallapur and **Niranjan N.Chiplunkar**, “Topology aware Mobile agent for efficient data collection in wireless sensor networks with dynamic deadlines”, International Conference on Advances in Computer Engineering, 20-21<sup>st</sup> June 2010, Bangalore, DOI: <http://doi.ieeecomputersociety.org/10.1109/ACE.2010.26>

18. Yuvaraju and **Niranjan Chiplunkar**, “PICCO: Protocol Independent Congestion control Method using Mobile relays for Adhoc Networks” , Proceedings of ICCNT, Bangkok **Thailand** 24<sup>th</sup> April 2010, pp 93-97
19. Ashok Kumar, **Niranjan Chiplunkar** and Goverdhan Hegde , “Sequential and Simultaneous Router for 3D FPGA”, **IJCSNS International Journal** of Computer science and network security (**IJCSNS**), Vol9, nO.2, Feb 2009 pp258-263
20. Ashok Kumar, **Niranjan Chiplunkar** and Goverdhan Hegde “Detailed Router for #D FPGA using Prioritized Auction Based Algorithm” **International Journal** of computer Science and Knowledge engineering (**IJCSKE**) , vol 3 No.1, Jan-June 2009, pp97-100
21. Ashok Kumar, **Niranjan chiplunkar** and Vinay “Detailed router for 3D FPGA using Sequential and simultaneous approach” Proceedings of International Multiconference of Engineers and Computer Scientists 2009, vol-II, IMECS, March18-20, 2009, **HongKong**, ISBN :978-988-17012-7-5
22. Sanju, **Niranjan N.Chiplunkar**, Routing Through Network On Chip, International Conference on “Recent Trends in Computational Science”, Toc-H Institute of Science & Technology, Kochi , Kerala, on June 11- 13, 2008
23. Sanju & **Niranjan N.Chiplunkar** “Network On Chip: A Brief Review” At International Conference ON ‘Emerging Techniques in Computing, Electronics, Embedded System & VLSI Design’ ICEVD 2008 Conducted By Padmashri Dr. Vithalrao Vikhe Patil College Of Engineering, Ahmednagar, From 20<sup>th</sup> Mar 2008 To 21<sup>th</sup> Mar 2008
24. **Niranjan N. Chiplunkar**, Ramakrishna, 2006. The role of Information Technology in exchange and management of knowledge by the Corporate and Institutions. **Accepted for presentation** at IEEE International Engineering Management Conference 2006 at Salvador, **Brazil**, 17<sup>th</sup> - 20<sup>th</sup> Sep, 2006.

25. Ashok Kumar & **Niranjan Chiplunkar** “ABR: Auction Based Router for 3D FPGA”, First International conference on “Frontier Technologies- Need for Business, Industry and Education”, Adhiyamaan College of Engineering, Hosur, TN, September 2006
26. Yuvaraju, Manjunath Kotari, **Niranjan N. Chiplunkar.**,2006. Security issues in wireless network, Proceedings of 3<sup>rd</sup> International Conference ObCom-2006 on Mobile, Ubiquitous & Pervasive Computing, Dec. 16<sup>th</sup> - 19<sup>th</sup> , 2006, VIT,Vellore, India. Volume-II, 210-212pp.
27. H.R.Nagesh, K.Chandrashekharan and **Niranjan N.Chiplunkar.** SURAKSHA: An implementation of Secure Group Communications. Proc. Of 12<sup>th</sup> International conference on “Advanced Computing and Communications, Nirma Inst. Of Technology, Ahmedabad. 286-294pp.
- 28. Niranjan N. Chiplunkar** & Chitrasena Bhat, Heuristic technology mapper for LUT based FPGAs. Proc. Of IEEE’s 12<sup>th</sup> International Conf. On VLSI Design, Goa,India Jan.1999. 390-393pp. Published in ACM portal by IEEE Computer Society, Washington, DC, USA. ISBN:0-7695-0013-7. DOI Bookmark: <http://doi.ieeecomputersociety.org/10.1109/ICVD.1999.745187>

## 16.2 National

- 29.** Srinivas Pai and Niranjan Chiplunkar “ Understanding the influence of faculty research on student’s learning and performance” ISTE National Convention 2014, College of Engineering Trivandrum, December 2014.
- 30.** Manjunath Kothari, Dr.HRNagesh and **Dr.Niranjan** “Security aspects of adaptive distributed systems and proposed solutions”, in ANUSANDHANA , Journal of Science,Engineering and Management, Vol 1, issue 1, March 2012 ISSN: 2231-4032, NMIT, Bangalore pp 45-49

31. Yuvaraju and **Niranjan Chiplunkar**, “Security issues in wireless Data Connectivity for the Library Information system, IT Policy and enforcement by Cyber Law”, Proceedings of National Seminar on “In search of information Power – future Challenges for Techno Libraries” held at NMAMIT, Nitte September 11<sup>th</sup> and 12<sup>th</sup> 2008
32. Ashok Kumar, **Niranjan Chiplunkar** and Prasanna Kumar . “Techno Libraries: A User Perspective”, Proceedings of National Seminar on “In search of information Power – future Challenges for Techno Libraries” held at NMAMIT, Nitte September 11<sup>th</sup> and 12<sup>th</sup> 2008
33. Ashok Kumar & **Niranjan Chiplunkar** “ Auction based Detailed router for 3D FPGA” , NC-Vcom SAINT, GITS college of Engineering, Kerala, March 2008
34. Ashok Kumar & **Niranjan N.Chiplunkar** “Auction Based Detailed Router Implementation for 3D FPGA”, NCCT’08, Mepco Schlenk Engg. College, Sivakash, March 2008
35. Sanju & **Niranjan Chiplunkar** “Network On Chip: The Gennext Technology For Billion Transistor Fabrication - A Review” At National Conference On Ubiquitous & Pervasive Computing Conducted By NMAMIT, Nitte From 24<sup>th</sup> Jan 2008 To 25<sup>th</sup> Jan 2008
36. **Niranjan N. Chiplunkar** & B Neelima on “ Analysis of Threaded Applications on Multi-core Platform”, 2008. At the TEQIP sponsored National Level Conference on “ National Conference on Ubiquitous and Pervasive Computing” organized by Department of Computer Science & Engineering, NMAMIT, Nitte, Karkala, Karnataka. 24<sup>th</sup> and 25 th January, 2008.
37. Ashok Kumar & **Niranjan Chiplunkar** “Auction Based Detailed Router for 3D FPGA”, IEEE VDAT 2007: Researches’ Forum, Kolkata, August 2007

- 38. Srinivas Pai & Niranjan N. Chiplunkar** "Effectiveness of E-learning as an Alternate new Paradigm Complementing Traditional Learning in Technical Education", The **Indian Journal of Technical Education**, Vol 29, No.4, October-December 2006, 23-29pp
- 39. Ashok Kumar, Ahmed Shafiq, Niranjan N. Chiplunkar**, "Auction based detailed router for 3D FPGAs", National Conference for PG & PhD Scholars –PGFEST 2006, NMAMIT, Nitte 28-29<sup>th</sup> April 2006. 22pp.
- 40. Yuvaraju B.N. & Niranjan N. Chiplunkar** "Securing Mobile Adhoc Networks with Bioinformatics", Proceedings of Asian conference on Intelligent Systems and Networks AISN 2006, Jagadhri, Haryana, 24-25<sup>th</sup>, Feb. 2006, Paper ID: AISN/CAT/101/94
- 41. Yuvaraju B.N & Niranjan N. Chiplunkar**, "Security in Manets using Bioinformatics" State Level Symposium on "Recent Trends in Biotechnology", NMAMIT, Nitte 15,16<sup>th</sup> Sept. 2005 "Parichay".9pp.
- 42. Srinivas Pai, Niranjan N. Chiplunkar**, "Role of information technology in enhancing the effectiveness of continuing education" 36<sup>th</sup> ISTE Annual Convention and National Seminar, 14-16<sup>th</sup> Dec. 2006, Bannari Amman Inst. Of Tech., Satyamangala, T.N., India. 28pp.
- 43. Ashok Kumar & Niranjan Chiplunkar** , "Auction Based Detailed Router for 3D FPGA", PGFEST'05, NMAMIT, Nitte, April 2005
- 44. Niranjan N. Chiplunkar**, "Distributed computing and JAVA", Proc. Of National Conference on Distributed Computing, NCDC-2004, NMAMIT, Nitte. 183-187pp.

45. Ganapathi Hegde and **Niranjan N. Chiplunkar**, "Application and Management of reconfigurable computing using operating systems", Proc. Of National Conference on Distributed Computing, NCDC-2004, NMAMIT, Nitte. 168-173pp.
46. Ashok Kumar, **Niranjan N. Chiplunkar**, and Ganapathi Hegde, " Distributed Computing in VLSI", Proc. Of National Conference on "Control, Communication and Information Systems: Volume II", 23-24<sup>th</sup> Jan. 2004, Farmagudi, Goa. 20-24pp.
47. Ganapathi Hegde and **Niranjan N. Chiplunkar**, "High level partitioning with synthesis for Multi-FPGA systems", Proc. Of National Conference on "Control, Communication and Information Systems: Volume II", 23-24<sup>th</sup> Jan. 2004, Farmagudi, Goa. 9-13pp.
48. **Niranjan N. Chiplunkar**, "RMI and CORBA –a framework for Distributed Computing application Development", Proc. Of National Conference on "Control, Communication and Information Systems: Volume II", 23-24<sup>th</sup> Jan. 2004, Farmagudi, Goa.1-5pp.
49. Nagesh H.R. , K.Chandrashekhara, and **Niranjan N. Chiplunkar**, "Design Implementation and analysis of Group communication using key graphs", Proc. Of National conf. on "Recent Trends in Network Technology", 13-15<sup>th</sup> Dec. 2003, Karpagam Engg. College, Coimbatore.
50. Ashok Kumar & **Niranjan Chiplunkar**, "Low Power FPGAs", PGFEST'03, NMAMIT, Nitte, 25-26 July 2003
51. Ganapathi Hegde & **Niranjan N. Chiplunkar**, "Multi FPGA system and its applications", Proc. Of PGFEST 2003, 25-26th July 2003, NMAMIT, Nitte. 35pp.

52. **Niranjan N. Chiplunkar**, "E-Learning:Use of IT in Teaching Learning Process", Proceedings of 32<sup>nd</sup> National Convention of ISTE(Indian Society for Technical Education), 27-29, Dec.2002. 62-63pp.
53. **Niranjan N. Chiplunkar**, "Review of Spatial Data Bases", Proc. Of National Conference on Recent Trends in DBMS, concepts & Practice, Nitte,India, March 2002. 209-216pp.
54. **Niranjan N. Chiplunkar**, Manoj V.N. & Manjaiya, "Design of High speed RSA cryptor Chip", Abstract. Of National Conf. On Signals, systems and Security, PSG Tech., Coimbatore,India, March 2002. 32pp.
55. **Niranjan N. Chiplunkar**, Manoj V.N. & Manjaiya, "Application of Chinese Remainder Theorem in designing a High sped RSA crypto chip", Proc. Of 2<sup>nd</sup> National Conference on Advanced Computing, PSG Tech, Coimbatore,India, Feb. 2002. 37-46pp.
56. **Niranjan N. Chiplunkar** & Manoj V.N, "Computational Aspects of certain Placement and Routing algorithms of VLSI system", Proc. Of 2<sup>nd</sup> National Conference on Advanced Computing, PSG Tech, Coimbatore, India, Feb. 2002. 51-55pp.
57. **Niranjan N. Chiplunkar** & Chitrasena Bhat, "Algorithm for Area Optimization in LookupTable based FPGA Technology mapping", Proc. Of annual CSI convention, NewDelhi, Sept. 1998. 149-155pp.
58. **Niranjan N. Chiplunkar**, "Technology mapping in FPGAs", Proc. Of National Conference on IT for 21<sup>st</sup> Century, Surathkal, India, 19-21st Dec.1996. 32-38pp.
59. **Niranjan N. Chiplunkar**, "Layout Compaction of VLSI circuits –an overview", MIT Technical Review journal, Manipal, vol. 4 Dec 1996. 58-64pp.

**60. Niranjan N. Chiplunkar**, “CAD for VLSI design”, CSI Communications Magazine , June 1993. 11-14pp.

**61. Niranjan N. Chiplunkar**, “CAD for VLSI”, The IEEE Annual, Journal, MIT, Manipal, May 1992. 25-27pp.

### **17. Books & Proceedings**

- “*VLSI CAD*” Text book – Niranjan N.Chiplunkar & Manjunath Kothari, PHI Learning New Delhi, May 2011 (ISBN: 978-81-203-4286-6)
- “*CAD for VLSI*” Text Book- **Niranjan N. Chiplunkar** & Manjunath Kothari, **NMAMIT Publication**, February, 2007 (ISBN : 978-81-7525-820-4)
- Member of Editorial Board of NMAMIT Annual Research Journal (ISSN: 2249-0426)
- Edited “*Proceedings of 32<sup>nd</sup> National Convention of Indian Society for Technical Education*” , B.S.Samaga, **Niranjan N. Chiplunkar**, Dec. 2002
- Edited “Proceedings of International conference & Symposium on Storage Technologies (ICSST)” **Niranjan Chiplunkar**, Mr.Manjunath Kothari, 2008 (ISBN: 978-81-907846-0-3)
- Edited “Proceedings of International conference on Computer Architecture, Networking and Applications (ICCANA)” **Niranjan Chiplunkar**, Dr.Uday Kumar Shenoy , 2011(ISBN: 93-80697-42-2), published by Excel India Publishers, New Delhi

### **18. Research Grants Obtained:**

- Intel-FICE academic fund of Rs.2.00lakh for setting up “Embedded Application lab. Using Intel ATOM processor” November 2011 . Also 10 Intel ATOM kits have been received costing about Rs. 2 lakhs.
- DST Project titled “Software architecture and programming model to enhance the performance of application on multi-core platform”



(DST/TDD/03) for 2 years from Jan 1st 2010 to Dec. 31<sup>st</sup> 2011. Amount sanctioned :Rs.16.00 lakhs(As Principal Investigator)

- AICTE-RPS(B) project titled “Study and design of algorithms for VLSI CAD tool development towards applications involving mutually exclusively modules” for 2 years from Dec. 1<sup>st</sup> 2008 to 30<sup>th</sup> November 2010, Amount sanctioned: Rs.4.6lakhs (As Co-Principal Investigator)

## 19. Research Interests

Algorithm design for VLSI CAD, Multi core architecture and Program Optimization, Ad-hoc Networks, E-learning, Advanced Microprocessors

## 20. Subjects Taught

Logic Design, Design and Analysis of Algorithms, Finite Automata & formal Languages, Computer Organization & Architecture, Systems Programming, RDBMS, Microprocessors & Peripherals, Principles of Programming Languages, compiler Design, Computer Graphics, VLSI Design, C# Programming & .NET Concepts, Embedded and Real Time Systems, Cryptography and Network Security, MIS, CAD for VLSI, System simulation and Modelling

## 21. Miscellaneous

- Developed “Computer Graphics Laboratory” at Manipal Institute Technology, Manipal, India in March 1988
- Reviewer for the Proceedings of IEEE Conference on “VLSI Design” since 2002.
- Chair of Programme Committee, National Conference on “Distributed Computing” , NMAMIT, Nitte India, 2004
- Coordinator for *FIVE* ISTE sponsored Short Term Training Programs
- Set up “Data Mining Laboratory” at NMAM Institute of Technology, Nitte during Jan. 2006
- Chair of Organizing Committee , National Conference on “Ubiquitous & Pervasive Computing” , NMAMIT, Nitte, India, 24-25, Jan, 2007
- So far, guided 42 BE level projects and 17 M.Tech. level projects
- President of Rotary Club of Nitte during 2006-07
- Consultant for a Patent Search organization :EntellectLLP.com

- **Applied for an Indian Patent**, based on research work (application no.: 428/CHE/2008)
- **Applied for US Provisional Patent** in April 2010 for the research work on “New Architecture for Network on chip (NoC) systems” (Application Number:61/395,480 dated 14/5/2010)
- Received AICTE travel grant for attending the conference at Shanghai, China
- Was Chairman of BOE (Board of Examination)–Computer Science at Mangalore University during 1997-98. Was member of BOE-Computer Science of Visvesvaraya Technological University ,Belgaum during 2004-05.
- BOS and BOE Chairman of NMAMIT Nitte for Computer Science UG and PG programs under Autonomous Scheme.
- Board of Studies member of Computer Science at MIT, Manipal University, NMIT Bangalore, SDMCE Dharwad, BVBCCE Hubli, Bangalore, Dr.AIT Bangalore and St.Aloysius College Mangalore during 2007-08
- Governing Council Member of St.John Engg. College Bangalore, Academic Council Member of Dr.AIT Bangalore from 2013.
- President of Hockey Udupi from 2014 July

## **22. Seminars/conferences /Continuing education programs Attended:**

### International:

- EMC World conference at Las Vegas USA 19-22 May 2008
- Conference on Computer Networking, Bangkok, Thailand, 24<sup>th</sup> April 2010
- Conference on Computer Research and Development , Shanghai, China , 10-12March 2011

### National

- Ten days Faculty development program at IISc on “Computer Graphics and CAD” during April 1988

- Three days training program on “Software Engineering” at Infosys Bangalore during August 1992. Resource Person :Prof. H.N.Mahabala
- Five days STTP on “Advanced JAVA at PSG, coimbatore during 2001
- Five days STTP on “COM & DCOM” at NITK, Surathkal during November 2005
- Three days TEQIP Workshop on Financial Management at Indian Institute of Financial Management at Faridabad during November 2006
- Five days IUCEE workshop on “Advanced compilation Technique” at Infosys campus Mysore during June 2008. Resource person: Prof. Jack Davidson, from Virginia Tech., USA
- Five days IUCEE workshop on “Advanced Microprocessors” at Infosys campus, Mysore during June 2009. Resource person : Prof. Rafiqzmann, California Polytechnic at Pamola
- Many more one day/two day programs at NMAMIT Nitte, MIT Manipal , NITK surathkal, MSRIT Bangalore and other colleges

**23. Seminars/Conferences/continuing education programs Organized:**

- AICTE-STTP on “Theory & Practice of CAD for VLSI” – 2-14<sup>th</sup> Sept. 2002
- AICTE-ISTE Winter school on “Object Oriented and visual programming” 17-29<sup>th</sup> Jan 2000
- AICTE-ISTE STTP on “E-learning, XML and Multimedia”- 9-21<sup>st</sup> Feb 2004
- National Conference on “Mobile and Pervasive computing”, 16-17 Sept. 2009
- 22<sup>nd</sup> CSI Karnataka Student Convention- 7<sup>th</sup> Nov. 2008
- National Conference on Ubiquitous computing, in 2006
- National level Workshop on “Device Drivers on Multiplatforms” in 2007

- International Conference on “Storage Technologies” 3-4 Dec. 2008
- International conference on “Computer Architecture, Networking and Applications”, Jan 7-8, 2011
- International Conference on “Emerging Trends in Engineering”, May 2012
- Many more one day/two day programs at NMAMIT, Nitte for the teaching/non teaching staff of NMAMIT and other colleges.